

FARADAY

2013

Profile & FIN Data

June, 2013

Confidential



FTC Profile

Milestones

2010-2013

- Delivered the 340M gate-count SoC solution @ 40nm, proving the leading technology & service capabilities
- Listed at the WW top 50 fabless suppliers in Y2012 (36th ranking); ranked at 3rd by growth percentage (*Source: IC Insight, 2013*)

2006-2009

- The shipment of USB 2.0 achieved 200 millions, announced other HSIO of SATA2, PCIe2, MIPI, DDR3
- Received the Frost & Sullivan Award (USA, Y2007) for “Customer Service Innovation” in the area of VLSI design services

2000-2005

- Awarded “Outstanding Financial Performance Fabless Company” by FSA, Y2004 (now change to GSA)

1993-1999

- 1999: Listed in Taiwan Exchange (TAIEX:3035)
- 1993: Spun off from UMC, founded as the first fabless ASIC design service provider in ASIA

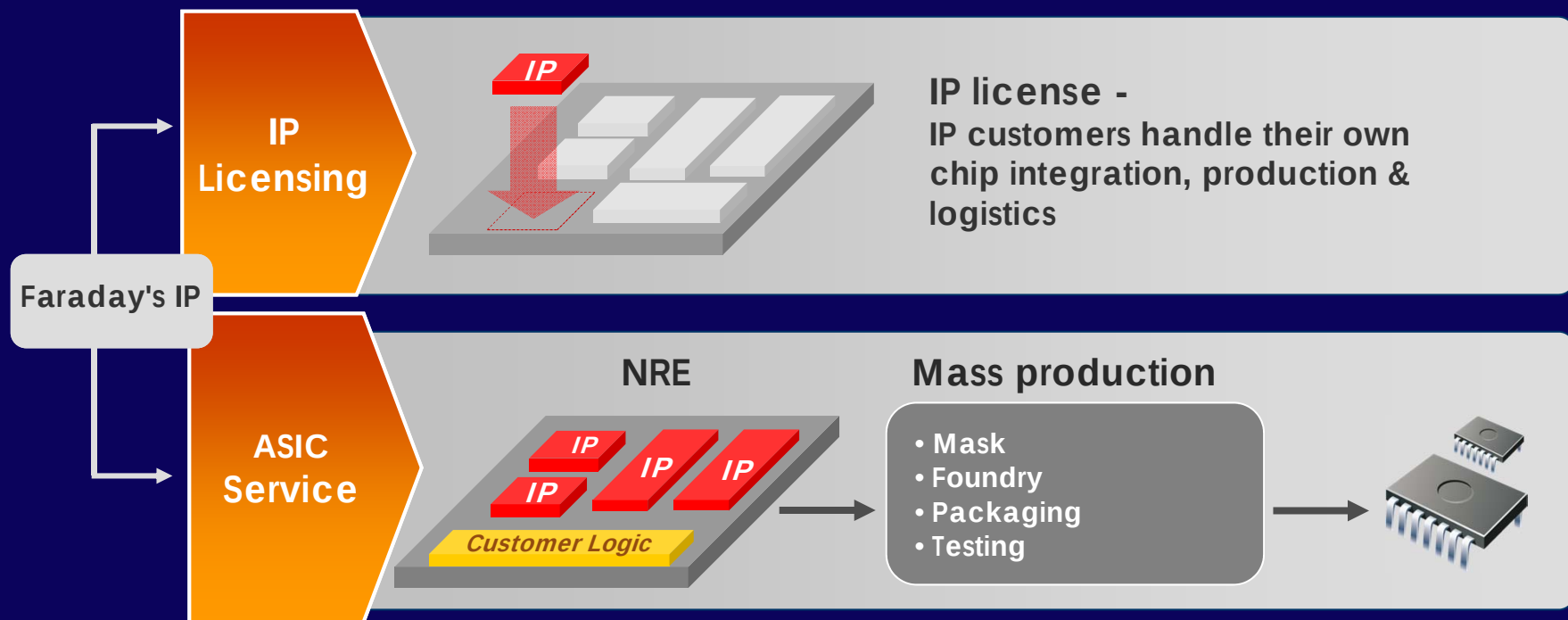


Faraday Global Presence

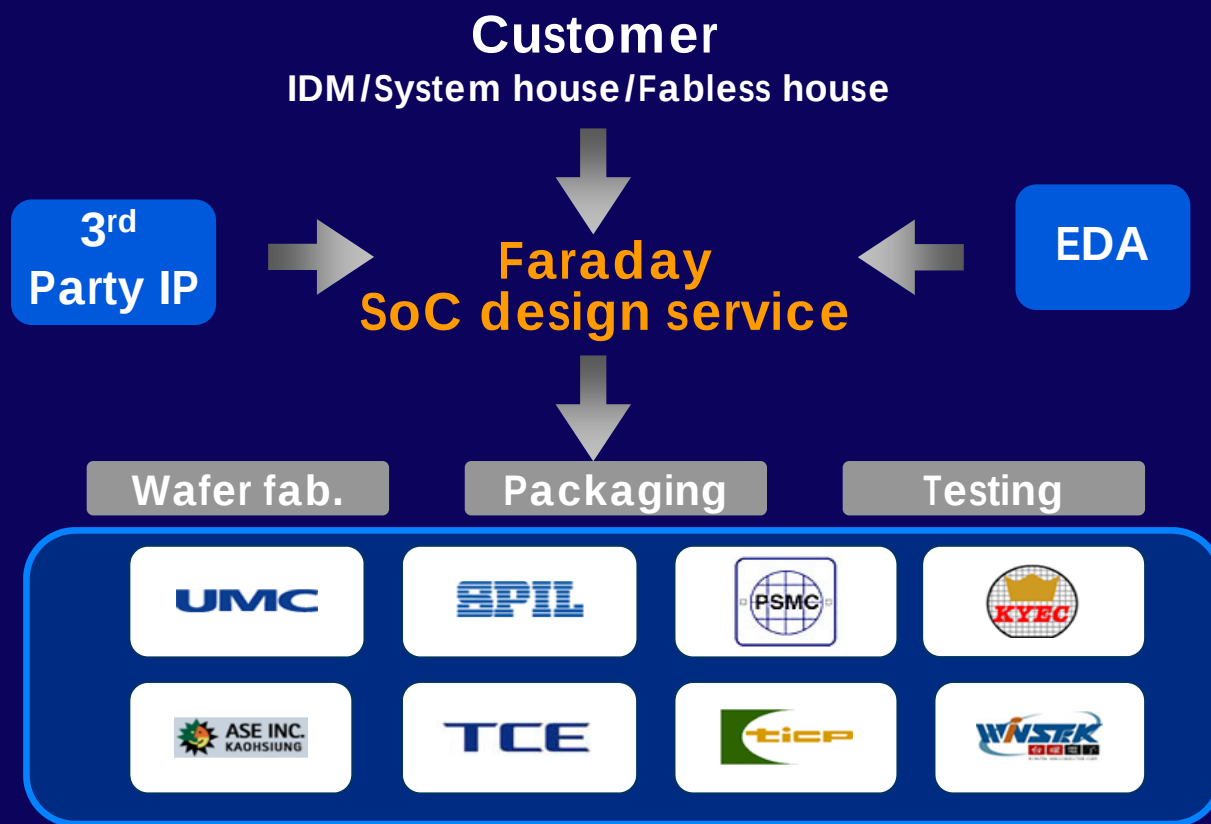


- 700+ employees worldwide, 550+ in R&D
- Headquarters in Hsinchu Taiwan; branches offices in China, USA, EU, JP, and Korea

Our Business



One-stop Shopping for Customers



- Close co-work with foundry to ensure process familiarity
- Multiple subcontractors for quality, efficiency and risk control

Leading Position in ASIC and IP

World wide

- **IC design service**
 - Among top 50 fabless IC suppliers **(No.36)***
- **Silicon IP**
 - WW top 10 vendor **(No.9)****

Ranking by revenue,

** IC Insight, Feb. 2013*

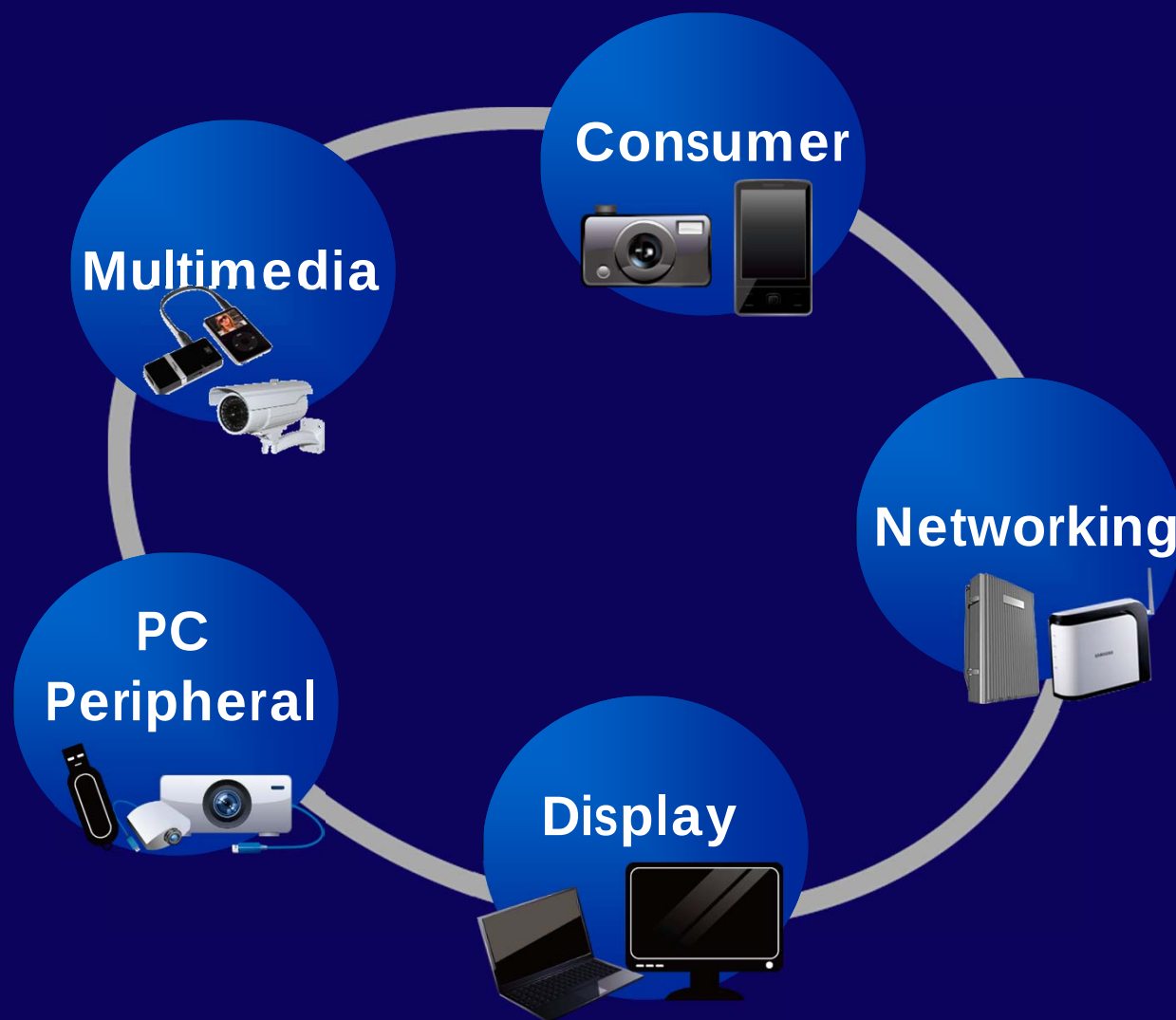
*** Gartner, March 2013*

Taiwan

- **IC design service**
 - Among top 10 fabless IC suppliers
- **Silicon IP**
 - Taiwan No.1 IP vendor



Wide Spectrum Successful Application



Comprehensive IP Pool

Faraday IPs

MEMORY

- Library / Memory
 - UMC 0.5um – 65/55/40 nm/28nm process standard cell libraries
 - miniLib™: Ultra high density & low power libraries
 - Embedded SRAM, ROM, e-Flash, 1T-SRAM, e-DRAM

DIGITAL

- Peripheral IPs/ Controllers/MPEG-4, H.264

HIGH-SPEED I/O

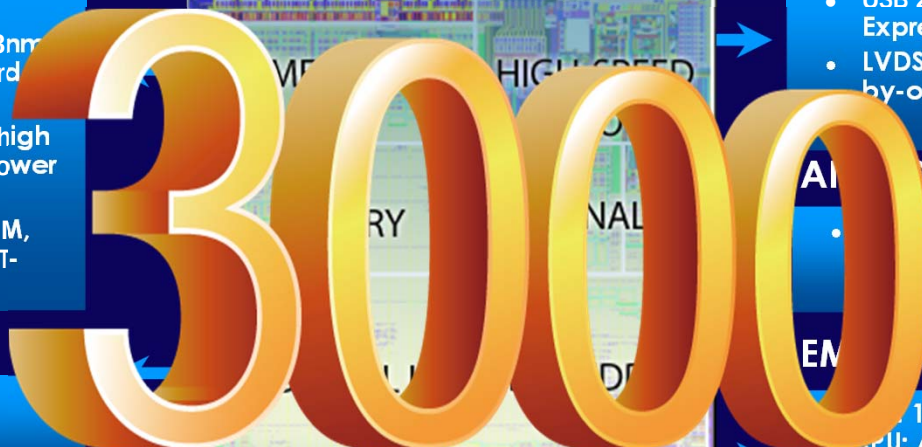
- USB 2.0/3.0, USB-OTG, PCI Express, SATA, Ethernet
- LVDS, mLVDS, PPML, eDP, V-by-one

ANALOG

- SCG PLL, DLL, OSC, REG, VDT, ADC, DAC, Sigma-ADC/DAC DDD

EMBEDDED CPU/DSP

- 16/24-bit DSP
- CPU: ARM7/ARM9/ARM Cortex series (M0, M3, A5, A9, etc.)/Faraday's FA510/FA526/FA626/FA726



Y2013Q1 Review

Statement of Comprehensive Income

Unit: NT\$ M

	'13Q1	'12Q4	QoQ	'12Q1	YoY
Net Operating Revenues	1,828	2,080	-12.1%	1,836	-0.4%
Gross Margin	769	791	-2.8%	773	-0.5%
Operating Expenses	(523)	(548)	-4.5%	(504)	3.9%
Operating Income	246	243	1.1%	269	-8.8%
Net Non-operating Income (Loss)	(8)	(2)	281.7%	(2)	361.8%
Income Tax Benefit(Expense)	(34)	(36)	-7.2%	(24)	38.0%
Net Income (Attributable to Parent)	205	207	-1.1%	244	-16.1%
EPS (NTD)	0.51	0.52		0.61	
%					
Gross Margin Rate	42.1%	38.0%		42.1%	
Operating Income Rate	13.4%	11.7%		14.7%	
Net Income Rate	11.2%	10.0%		13.3%	

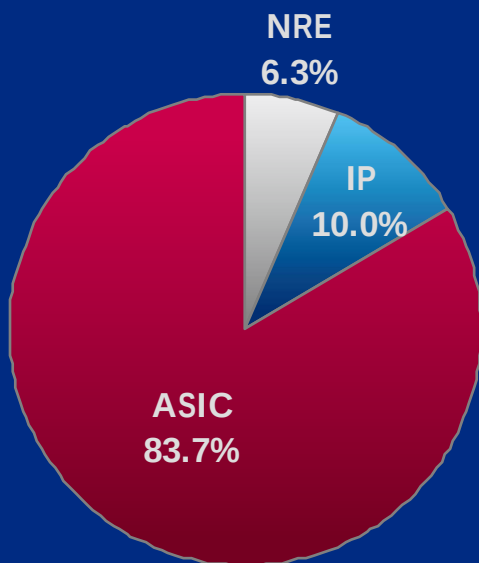
Condensed Balance Sheet

Unit: NT\$ M

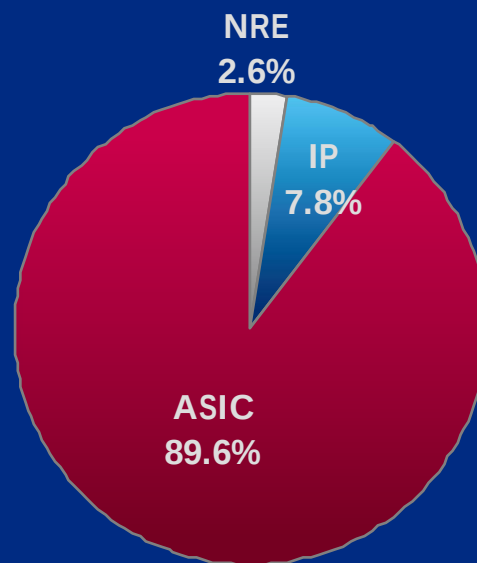
	'13Q1	'12Q4
Cash and Cash Equivalents	3,759	3,551
Notes and Accounts Receivable	987	1,229
Inventories	888	738
Total Assets	8,968	8,864
Notes and Accounts Payable	615	712
Current Liabilities	1,692	1,808
Total Liabilities	1,850	2,014
Stockholders' Equity	7,118	6,850

Sales Breakdown by Billing Method- QoQ

2013 Q1

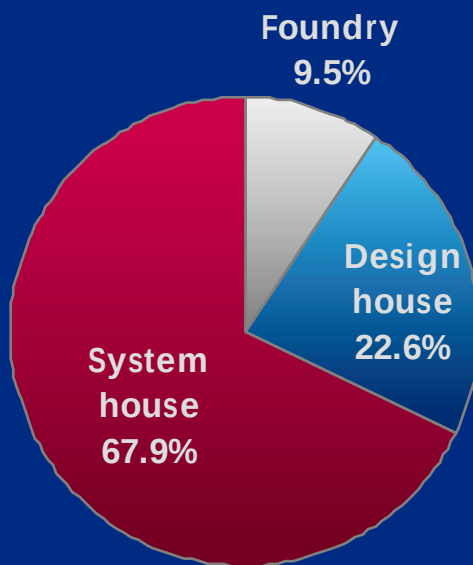


2012 Q4

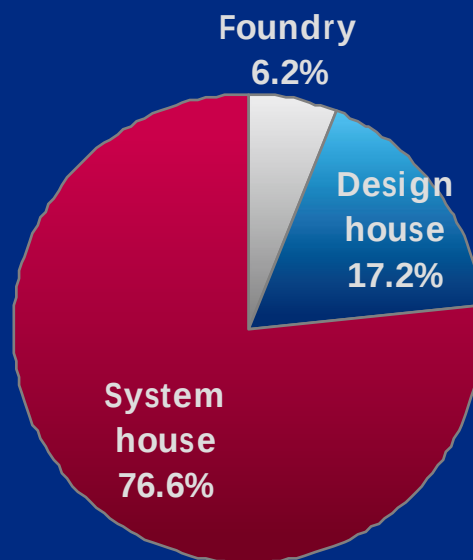


Sales Breakdown by Customer Type- QoQ

2013 Q1

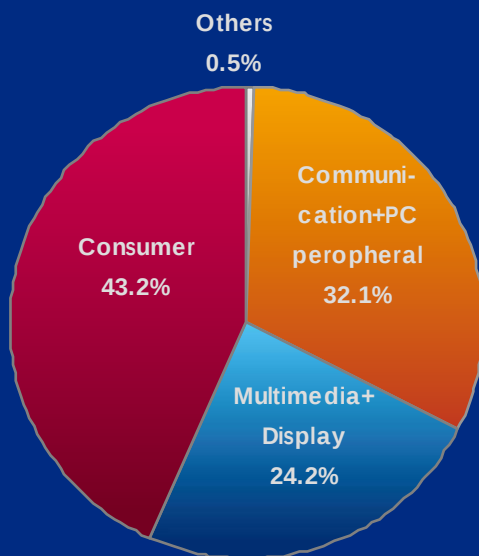


2012 Q4

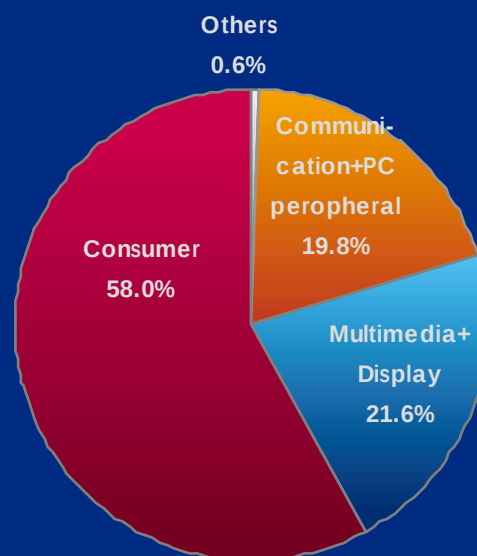


Sales Breakdown by Application- QoQ

2013 Q1

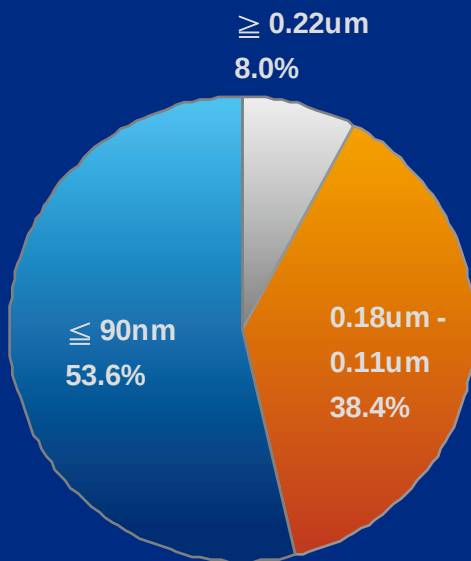


2012 Q4

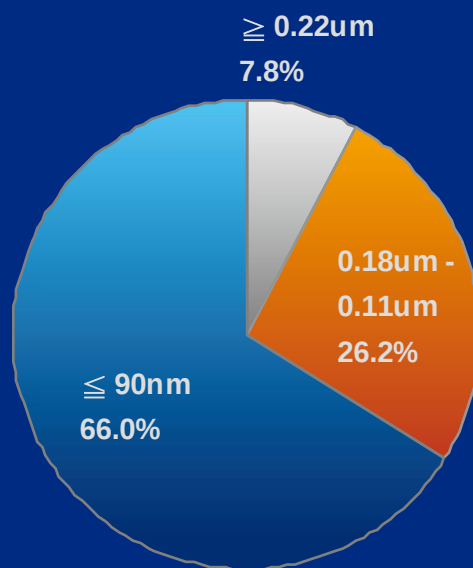


Sales Breakdown by Technology-QoQ

2013 Q1



2012 Q4



Thanks for Your Attention!
