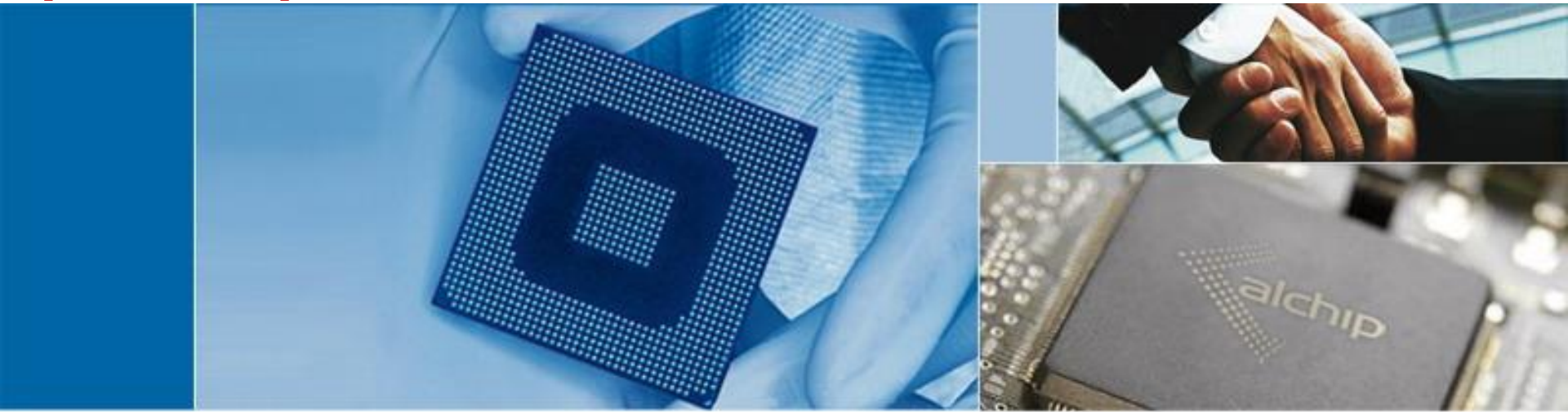




(3661 TT)



Alchip Technologies

Trusted Silicon Partner Realizing Innovations

Safe Harbor Disclaimer



This presentation contains forward-looking statements, including statements about business outlook and strategy, and statements about historical results that may suggest trends for our business. These statements are based on estimates and information available to us at the time of this presentation and are not guarantees of future performance. Actual results could differ materially from our current expectations as result of many factors, including: our financial performance, including our net revenue, cost of revenue, operating expenses and ability to sustain profitability; our planned capital expenditures; our ability to expand our customer base; our ability to expand our product and service offerings; the impact of seasonality on our business; our ability to remediate the material weaknesses and significant deficiencies in our internal control over financial reporting; our ability to stay abreast of modified or new laws applying to our business; and our spending of the net proceeds from this offering. Except as required by law, we undertake no obligation to update any forward-looking statements, whether as a result of new information, future events, or otherwise.

公司簡介

企業概況

創立日期：2003年2月

營業範圍：ASIC 設計服務生產

董事長：關建英

總經理：沈翔霖

員工人數：員工415人、工程師290人

資本額：6.16億台幣

中國

上海

上海市徐匯區龍華中路596號
綠地中心東樓11樓

無錫

無錫蠡園開發區建築西路777
號A5棟4F

合肥

安徽省合肥市望江西路800号
合肥創新產業園C4棟6樓

濟南

濟南市高新區齊魯軟件園2F

北京、廣州辦公室

台灣

台北

台北市內湖區文湖街12號9樓

新竹

新竹縣竹北市台元科技園區
台元一街1號11樓之一

日本

新橫濱

10F Shin-Yokohama Square Bldg, 2-3-
12 Shin-Yokohama, Kouhoku Yokohama
Kanagawa Japan

韓國

首爾

A-903, 559, Dowha-dong,
Mapo-gu, Seoul, 121-784, KOREA

北美

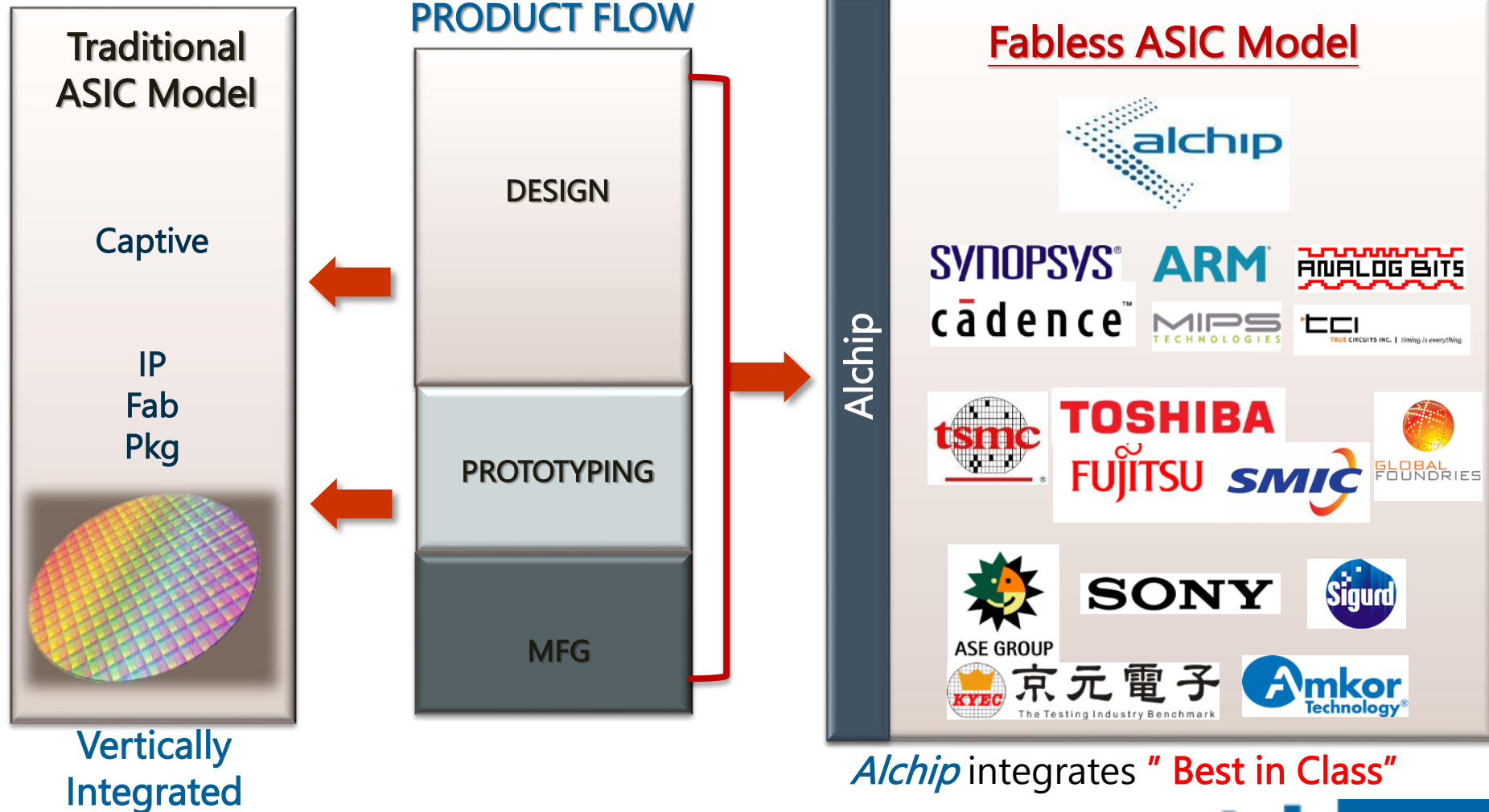
矽谷(銷售中心)

P. O. Box 5176 Santa Clara,
CA 95056-5176

ASIC晶片市場領域



商業模式



合作模式



Customer

Focus on own core competencies
(architecture/front-end development)

Alchip

Focus on *silicon design, prototyping
bring-up & manufacturing*



***Fast TTM &
Cost Competitive***



***Product
Ramping Success!***

your business success

Long term contract

Early stage involvement
Dedicated design team
Local support
Multiple project assignment
RTL/Netlist to MP

Technology solutions

Multiple ARM usages
Total IP Solutions
Special circuit customization
Advanced Packaging
Unmatched SoC Design Solutions

Manufacturing

Prototyping
MP
Full Supply chain management
Continuous cost reduction

經營實績



2007

2009

2011

2013

2015

2017

Low Power



- 90nm (2004)
- JP captive fab
- Portable gaming



- 90nm (2005)
- JP captive fab
- Gaming



- 65nm (2008)
- ARM11
- mDDR/sub-LVDS
- H.264
- HD Camcorder SoC



- 40nm (2009)
- Portable gaming



- 55nm (2010)
- LTE Base Band



- 28nm (2012)
- 65Mgate
- Mobile Game



- 55nm / 40nm (2013)
- Cell phone / Camera



- 40nm (2013)
- Camera

★ *World No.1 OEM of digital cameras market*



- 28nm (2016)
- Cell phone / Camera

- 40nm (2015)
- Cell phone / Camera



- 28nm (2016)
- Camcorder



High Performance



- 90nm (2006)
- JP captive fab
- HDTV



- 65nm (2008)
- PLL,ADC,EDRAM,OSC,SRAM
- HDTV
- 8 chips in HV MP



- 40nm/32nm (2012)
- TV engine
- Mobile Platform

- 90nm (2006)
- Super computer
- 60M gate count
- 500MHz
- 512 CPUs
- FCBGA, 60-watt/SoC



- 40nm (2012)
- 65nm (2010)
- Networking



- 55nm (2011)
- Tablet PC



- 28nm (2014)
- 76Mgate
- Entertainment



- 12nm/7nm (2017/18)
- 16nm/14nm(2015)
- 20nm(2014)
- 28nm(2013)
- Bitcoin Mining Machine



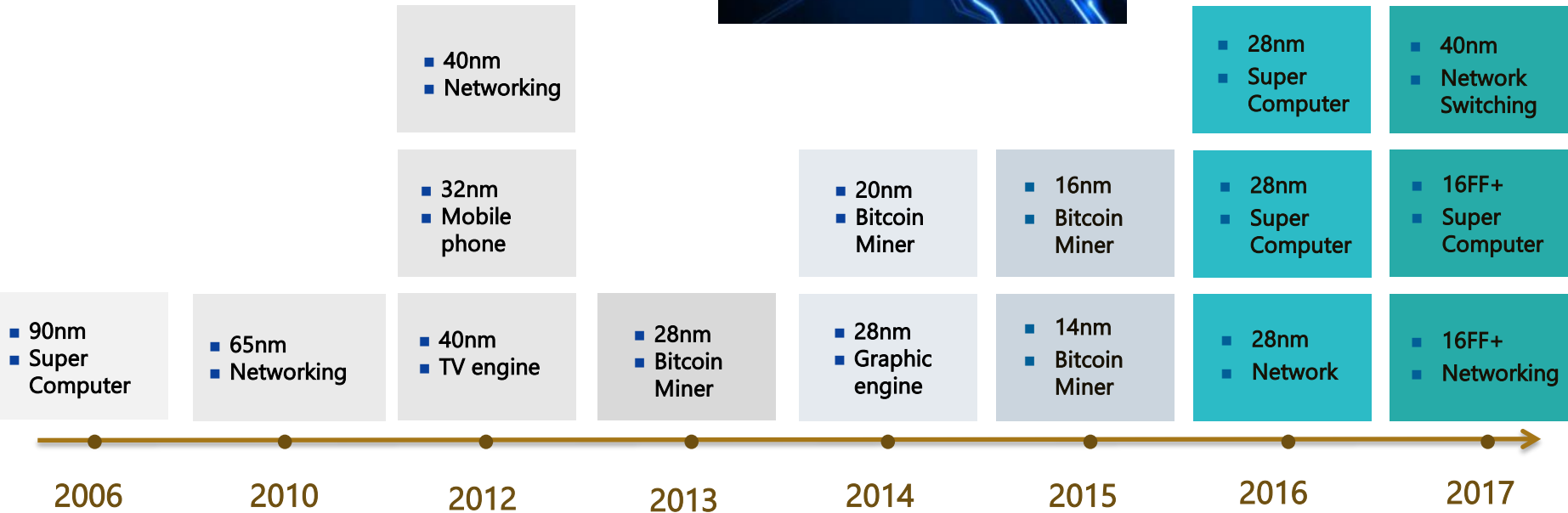
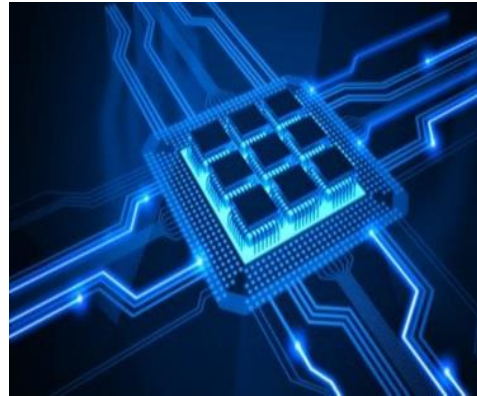
- N7 (2018)
- 16FF+ (2016)
- Super Computer
- 2GHz

經營實績 (高性能電腦)



20+ 流片

HPC related Design Projects



超級電腦研發歷史



The world's fastest supercomputer (2006)



Grape DR (2006)

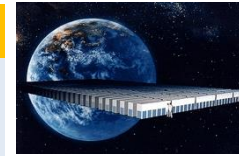
- TSMC 90nm
- Super computer
- 60M gate count
- 500MHz
- FCBGA, 60-watt/SoC



- TSMC 40nm (2012)
- Networking, core processor
- 50M gates

Grape X (2013)

- TSMC 28nm HP, 1P8M
- Super computer
- 3.1M gates
- 500MHz
- 512 CPUs
- PKG: PBGA



2018



- TSMC 28nm (2015)
- Networking

- TSMC16 FF+ (2017)
- Networking
- 460M gate count

- High performance
- High volume of memory
- High bandwidth memory access
- High bandwidth I/O access
- Large scale cache coherency maintained

2012

- TSMC 28nm HPM (2014)
- 471M gate count
- 733NHZ
- Medical Equipment

- TSMC 16nm FF+ (2016)
- 1521Mgates
- 1Ghz
- 120W power consumption



- TSMC 65nm (2010)
- Networking
- 60M gates
- 800MHz

2010

2008

2006

財務及營運資訊 & 未來展望

1H18單季損益



美金(千元)	1Q18	QoQ(%)	YoY(%)	2Q18	QoQ(%)	YoY(%)	1H18
營業收入	27,697	14	12	32,735	18	-15	60,432
營業成本	14,999	21	-15	17,591	17	-37	32,590
營業毛利	12,697	7	79	15,144	19	47	27,842
營業費用	8,577	-3	40	8,833	3	22	17,410
營業利益	4,120	36	341	6,311	53	108	10,432
業外收益	584	223	78	-1,308	n.m.	n.m.	-724
稅前利益	4,704	46	273	5,004	6	41	9,708
所得稅費用	-831	53	1198	-983	18	92	-1,814
稅後收益	3,873	45	223	4,021	4	32	7,894
EPS (NT\$)	1.85			1.95			3.80

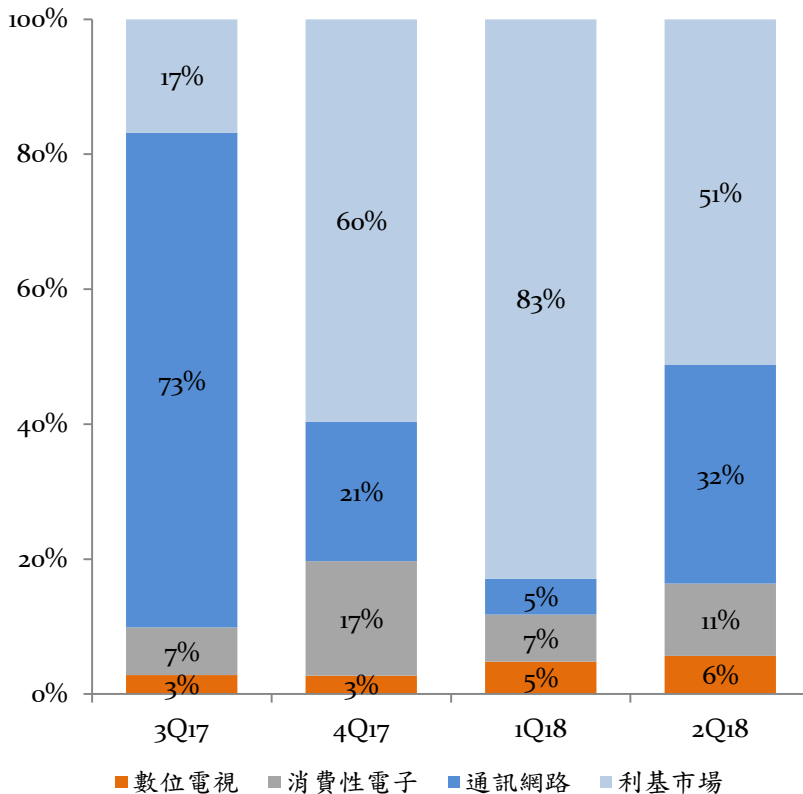
1H18營運回顧



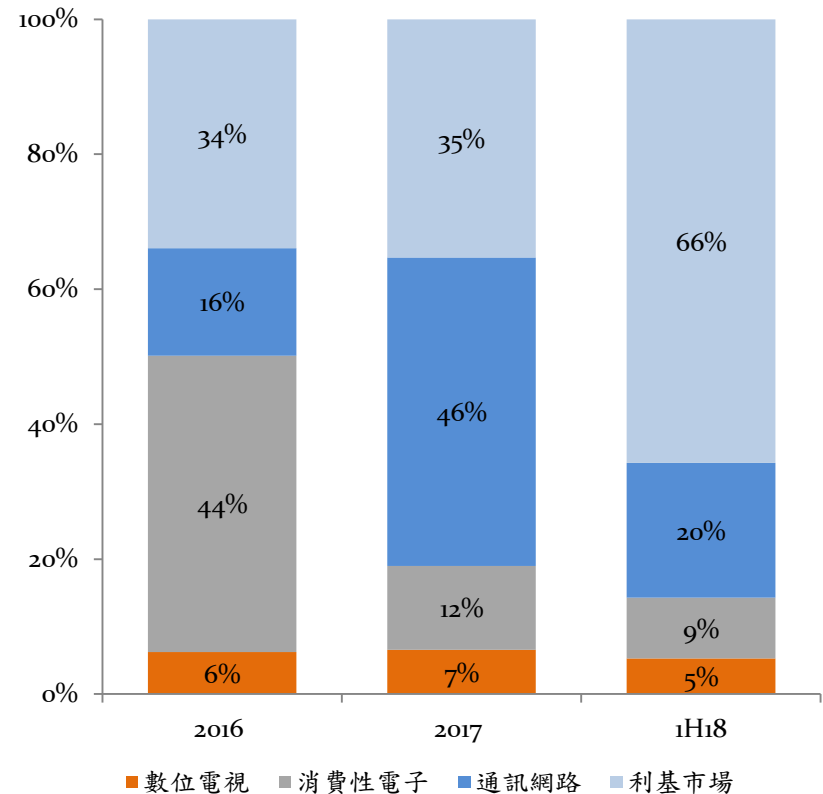
- **1H18營收為US\$60.4M，YoY微幅下滑4%**
 - 1H18主要營收動能仍來自HPC，貢獻主要來自於數位貨幣挖礦晶片
 - 受惠於設計占比持續拉高，1H18毛利率46.1%
- **2Q18營收US\$32.8M，YoY下滑15%，營業利益US\$6.3M，YoY成長108%**
 - YoY營收下滑主因為，相較於2Q17，2Q18無超算晶片量產營收貢獻
 - 2Q18主要營收貢獻仍來自於HPC之設計營收，設計及HPC營收比重超過五成
- **2Q18稅後收益為US\$4.02M，QoQ成長4%，YoY成長32%**
 - 稅後收益成長低於營業利益成長主因為業外投資損失，持有之固定收益產品認列約US\$1.6M投損

產品應用營收分布

單季應用營收分布

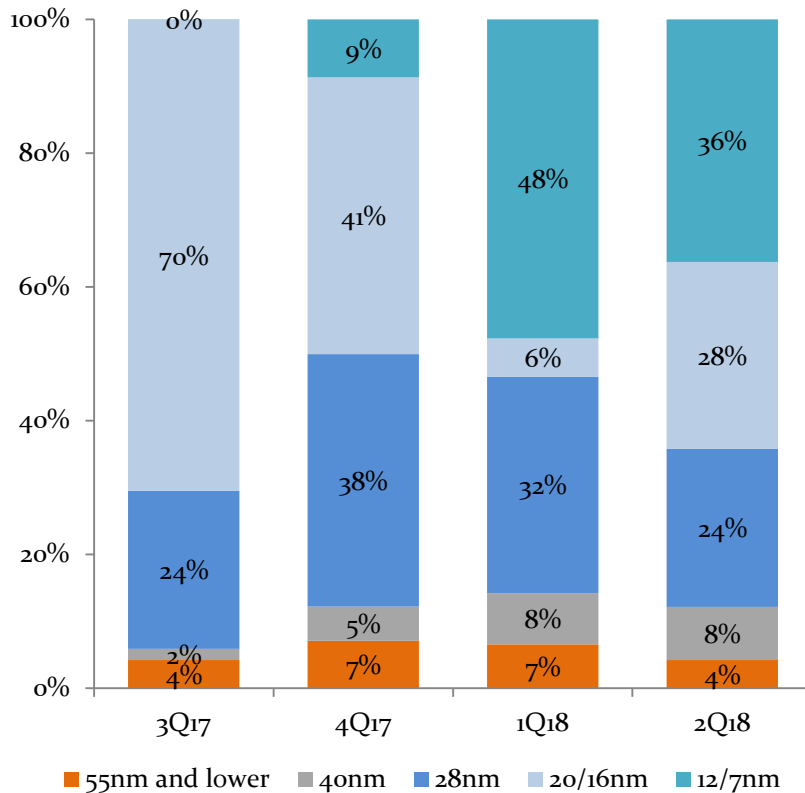


年度應用營收分布

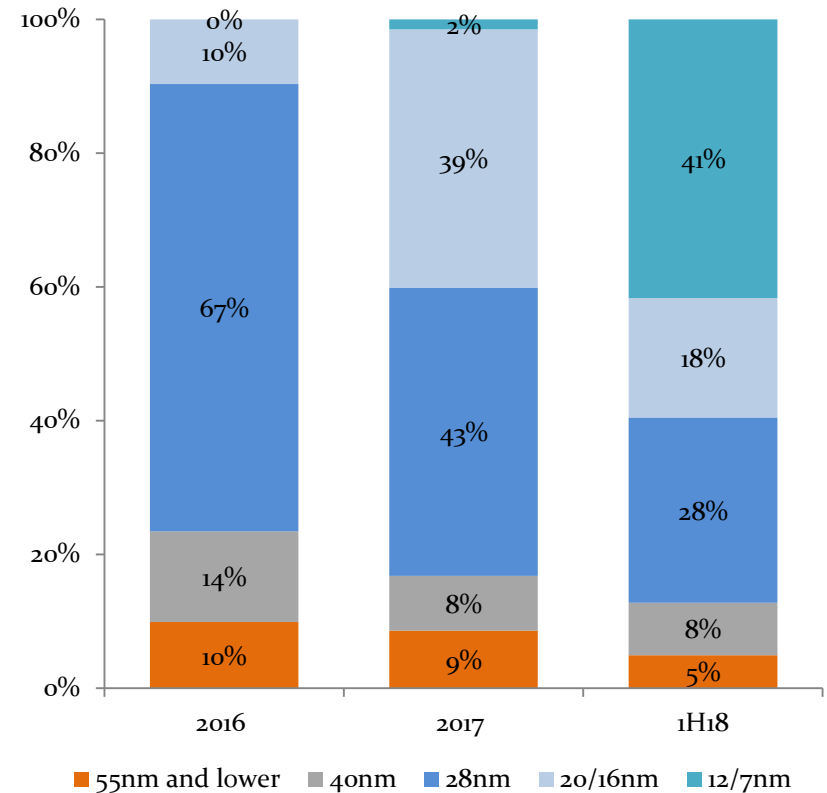


製程營收分布

單季製程營收分布



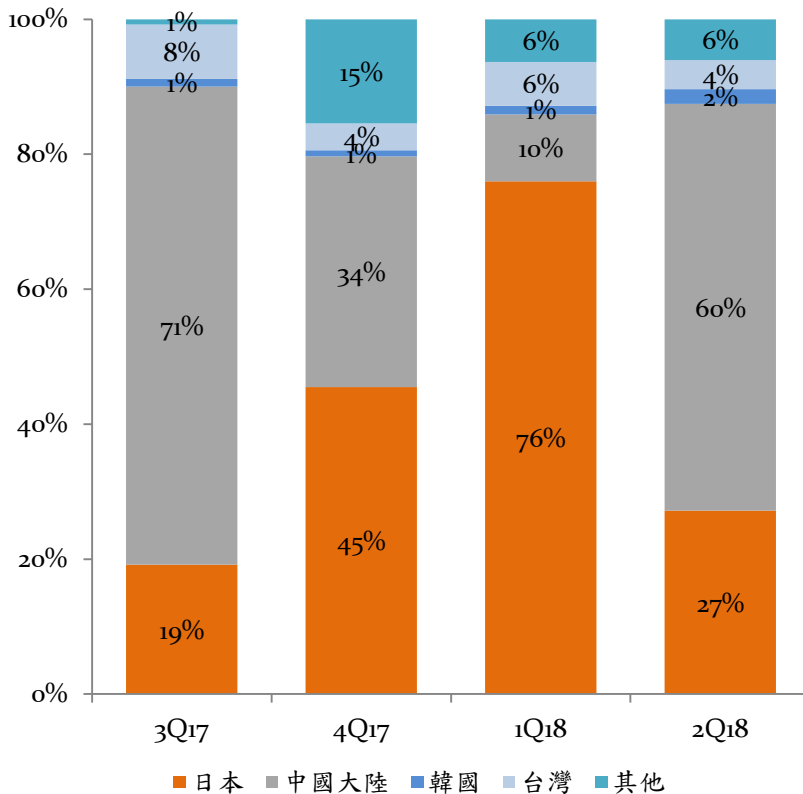
16-1H18年度製程營收分布



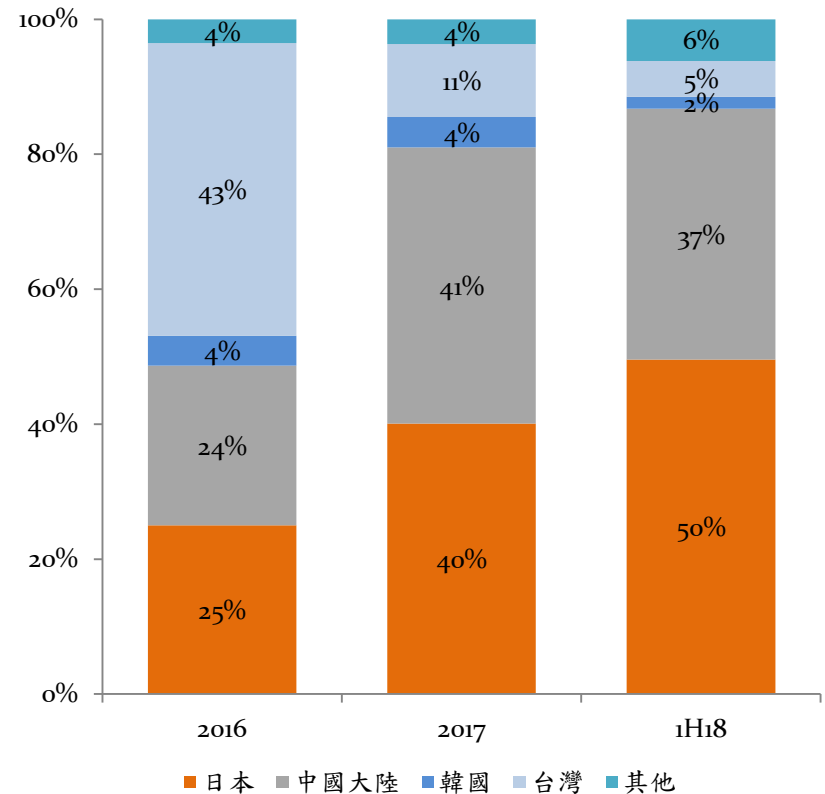
先進製程係指40nm或更先進之製程

地區營收分布

單季地域性營收分布



2016-1H18地域性營收分布

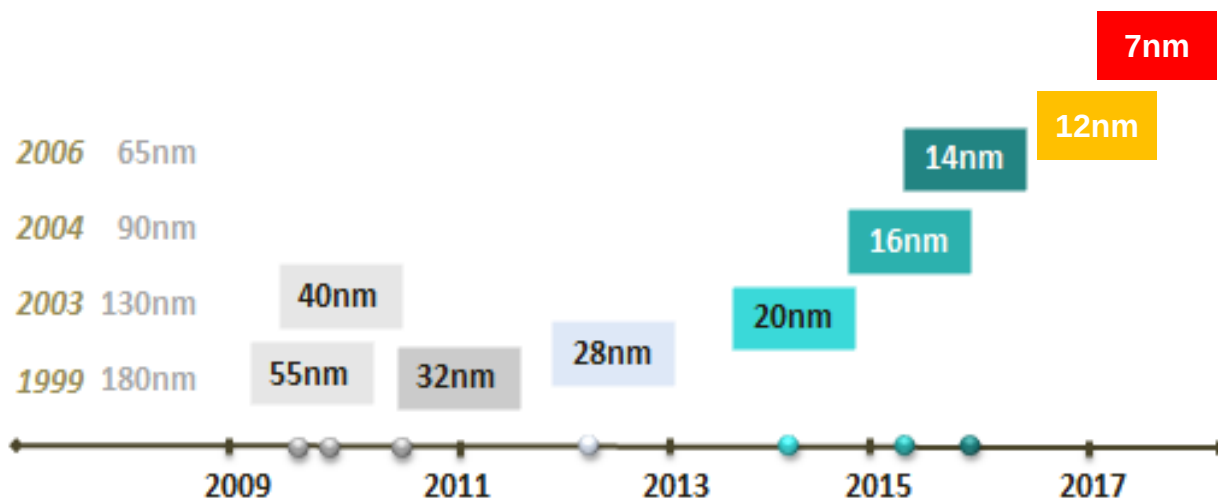


成功紀錄



世芯電子成立以來已成功流片超過300個設計案！

**350 +
Tape-Outs**





謝謝指教！

www.alchip.com