

ASX RELEASE

3 October 2019

4DS PROGRESSING TO IMEC'S MEGABIT MEMORY PLATFORM

- Completed analysis of the fourth iteration of 300mm wafers (being the 46 wafers in “Lot 4”)
- Results affirmed, and further enhanced, 4DS’ understanding of the process conditions required to reproduce its memory on state-of-the-art production equipment with the characteristics required for stand-alone Storage Class Memory
- Based on the results of Lot 4, the Company and imec have agreed to integrate 4DS’s memory with imec’s megabit platform – a significant milestone as 4DS moves towards its goal of demonstrating a 1 megabit memory chip
- Planning with respect to the integration has commenced, with work to be undertaken in Q4 of 2019

4DS Memory Limited (ASX:4DS) (**4DS** or the **Company**) refers to its ASX announcement dated 20 June 2019, and is pleased to provide this update on the status of the development of its memory technology.

During August and September 2019, the Company received and analysed the two sets of twenty-three 300mm wafers (46 in total) in Lot 4. This analysis, and the accumulated knowledge from prior Lots, has:

- further enhanced the Company’s understanding of the process conditions required to produce 4DS’ memory on imec’s state-of-the-art production equipment, being the same equipment as used by high volume/high density manufacturers of memory;
- affirmed the impact of changes in process conditions on cell structure and desired memory characteristics for stand-alone Storage Class Memory¹, with meaningful improvements having been achieved with each subsequent Lot of 300mm wafers; and
- provided further evidence of the repeatability of the process for producing 4DS’ memory on state-of-the-art production equipment

Following the analysis of the results of Lot 4, 4DS and imec have agreed to advance to the next development stage, being the integration of 4DS’ memory technology with imec’s megabit platform. This platform is a proven CMOS megabit test vehicle fabricated on 300mm wafers on which the 4DS memory cells can be placed and function. It will enable 4DS to demonstrate the true potential of its Interface Switching ReRAM technology for Storage Class Memory. The integration will be undertaken in back-end-of-line (BEOL) processes, which is the latter stage of the chip manufacturing process.

4DS and imec have commenced planning for the initial phase of the integration of 4DS’ memory with imec’s megabit platform. The Company will produce its initial lot of 300mm wafers during Q4 of 2019. The integration includes continuous monitoring and analysis of speed, endurance, retention and yield (in that order of importance).

The integration will extend into calendar year 2020, beyond the existing term of the collaboration agreement between 4DS and imec (31 December 2019).

¹ Notably speed, endurance and retention. See <http://www.4dsmemory.com/technology/reram> for the Company’s objectives and priorities with respect to the characteristics required for Storage Class Memory.

4DS is currently in discussions with imec regarding the terms of an extension to the collaboration agreement between the parties, and the Company expects to finalise and agree the terms of the extension in the coming weeks.

Megabit memories are the minimum size needed to collect the statistically significant and meaningful data on endurance, speed, data retention and yield that are essential for high-volume memory makers to make informed decisions.

Chief Executive Officer and Managing Director, Dr Guido Arnout, commented “Having fabricated and tested around one hundred 300mm wafers, over 4 iterations, we believe we know how to repeatedly produce our memory on state-of-the-art production equipment. We are excited to be progressing to imec’s megabit platform and look forward to ultimately producing a megabit memory chip”.

Chairman, Mr Jim Dorrian commented “The joint decision with imec to now integrate our technology with imec’s megabit platform represents a significant step forward, and is a credit to the hard work and dedication of Guido and his technical team. We are delighted with the continued support we receive from imec, as well as from our joint development partner, Western Digital.”

- ENDS -

Contact information

Investors: David McAuliffe
4DS Memory

+61 408 994 313
david@4dsmemory.com

About 4DS

4DS Memory Limited (ASX: 4DS), with facilities located in Silicon Valley, is a semiconductor development company of non-volatile memory technology, pioneering Interface Switching ReRAM for next generation gigabyte storage in mobile and cloud. Established in 2007, 4DS owns a patented IP portfolio, comprising 22 USA patents granted and 11 patent applications pending and or filed, which has been developed in-house to create high-density Storage Class Memory. 4DS has a joint development agreement with Western Digital subsidiary HGST, a global storage leader, which accelerates the evolution of 4DS’ technology. 4DS also collaborates with imec, a world-leading research and innovation hub in nanoelectronics and digital technologies. The combination of imec’s widely acclaimed leadership in microchip technology and profound software and information and communication technology expertise makes them unique.

For more information, please visit www.4dsmemory.com.

About imec

imec is the world-leading research and innovation hub in nanoelectronics and digital technologies. The combination of our widely acclaimed leadership in microchip technology and profound software and ICT expertise is what makes us unique. By leveraging our world-class infrastructure and local and global ecosystem of partners across a multitude of industries, we create ground breaking innovation in application domains such as healthcare, smart cities and mobility, logistics and manufacturing, energy and education.

As a trusted partner for companies, start-ups and universities we bring together close to 3,500 brilliant minds from over 75 nationalities. Imec is headquartered in Leuven, Belgium and also has distributed R&D groups at a number of Flemish universities, in the Netherlands, Taiwan, USA, China, and offices in India and Japan. In 2016, imec’s revenue (P&L) totalled 496 million euro. Further information on imec can be found at www.imec-int.com.

imec is a registered trademark for the activities of IMEC International (a legal entity set up under Belgian law as a "stichting van openbaar nut"), imec Belgium (IMEC vzw supported by the Flemish Government), imec the Netherlands (Stichting IMEC Nederland, part of Holst Centre which is supported by the Dutch Government), imec Taiwan (IMEC Taiwan Co.) and imec China (IMEC Microelectronics (Shanghai) Co. Ltd.) and imec India (Imec India Private Limited), imec Florida (IMEC USA nanoelectronics design center).

For more information, please visit www.imec.be.

Disclaimer

This release contains certain forward looking statements that are based on the Company's management's beliefs, assumptions and expectations and on information currently available to management. Such forward looking statements involve known and unknown risks, uncertainties, and other factors which may cause the actual results or performance of 4DS to be materially different from the results or performance expressed or implied by such forward looking statements. Such forward looking statements are based on numerous assumptions regarding the Company's present and future business strategies and the political and economic environment in which 4DS will operate in the future, which are subject to change without notice. Past performance is not necessarily a guide to future performance and no representation or warranty is made as to the likelihood of achievement or reasonableness of any forward looking statements or other forecast. To the full extent permitted by law, 4DS and its directors, officers, employees, advisers, agents and intermediaries disclaim any obligation or undertaking to release any updates or revisions to information to reflect any change in any of the information contained in this release (including, but not limited to, any assumptions or expectations set out in the release).

You should not place undue reliance on these forward- looking statements. Except as required by law or regulation (including the ASX Listing Rules) we undertake no obligation to update these forward- looking statements.