

2025 Annual General Meeting – Chair Address and CEO Presentation

24 November 2025 – **Weebit Nano (ASX: WBT)**, a leading developer of advanced memory technologies for the global semiconductor industry, attaches the address to shareholders, which will be delivered by David (Dadi) Perlmutter and Anne Templeman-Jones at the Company's 2025 Annual General Meeting. The hybrid meeting is being held today in Melbourne at 3pm AEDT.

The presentation slides being presented by CEO Coby Hanoch are also attached.

ENDS

Authorised for release by the Board of Weebit Nano Limited.

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About Weebit Nano Limited

Weebit Nano Ltd. is a leading developer and licensor of advanced semiconductor memory technology. The company's ground-breaking Resistive RAM (ReRAM) addresses the growing need for significantly higher performance and lower power memory solutions in a range of new electronic products such as Internet of Things (IoT) devices, smartphones, robotics, autonomous vehicles, 5G communications and artificial intelligence.

Weebit's ReRAM allows semiconductor memory elements to be significantly faster, less expensive, more reliable and more energy efficient than those using existing flash memory solutions. As it is based on fab-friendly materials, Weebit ReRAM can be integrated within existing flows and processes faster and easier than other emerging technologies, without requiring special equipment or large investments.

See: www.weebit-nano.com

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2025 ANNUAL GENERAL MEETING CHAIR'S ADDRESS

2025 marks a decade since Weebit Nano was established. In a relatively short time by industry standards, we've developed and commercialised early-stage research-level technology into an advanced, production-ready technology to replace embedded flash in next-generation applications.

Over the past year, Weebit continued to gain commercial traction. We licensed and taped-out our embedded ReRAM at Tier-1 Integrated Device Manufacturer onsemi, achieved our 2025 target of three customers integrating our technology in next-generation applications, and qualified our technology to automotive AEC-Q100 standards. This progress delivered a four-fold annual increase in revenues in FY25.

The team remains on track to complete qualification at DB HiTek before the end of the 2025 calendar year, and discussions with more than 20 foundries, IDMs, and product companies are ongoing.

Demand for faster, lower power and better performing Non-Volatile Memory – or NVM - is being fuelled by AI, automotive and advanced consumer electronics. ReRAM is emerging as the NVM of choice for such applications, offering a faster, cost-effective and more secure memory solution than embedded flash.

Globally, Weebit is the only independent provider of qualified ReRAM and one of just three companies with qualified ReRAM technology in the market. Our world-class team is focused solely on ReRAM and the goal of our ongoing investment in technology improvement is for Weebit to become the leader in this market.

Across the semiconductor industry, we're seeing significant investments in the USA, Europe and other countries, to bolster sovereign manufacturing capabilities. This includes investment in new U.S. manufacturing facilities and capabilities from TSMC, Samsung, Intel and Texas Instruments as well as a \$1 billion commitment from the UK government for its domestic capabilities.

Our enduring partnership with our technical partner, CEA-Leti, has been instrumental to our technical progress and ReRAM leadership. I'd like to thank CEA-Leti as well as commercial partners DB HiTek, onsemi and SkyWater for their collaboration and support. We look forward to continued co-operation in FY26 and beyond.

To support our transition to a more mature business, we welcomed highly regarded Anne Templeman-Jones as an independent Non-Executive Director and Deputy Chair. Anne has strengthened our governance practices, ensuring we have the right infrastructure and processes in place. This included the release of our inaugural ESG report, which highlights the environmental advantages of our embedded ReRAM over flash and other non-volatile memory technologies.

Weebit would not be where it is today without the dedication and hard work of our leadership and Board. I thank my fellow Directors and the entire Weebit team for their commitment to our commercial and technical targets over the past year.



Before I hand over to Deputy Chair Anne Templeman-Jones to say a few words, I'd like to thank our loyal and engaged shareholders for your continued support. Our exceptionally strong start to FY26 reinforces our belief that our best years are ahead of us. FY26 will see us secure additional licensing and design agreements, improving availability of our embedded ReRAM, growing revenues, and importantly, delivering value for shareholders.

2025 ANNUAL GENERAL MEETING DEPUTY CHAIR'S ADDRESS

Weebit Nano is on the cusp of significant growth with a large and growing market opportunity. Its essential memory technology is suitable for almost every industry and application, and will touch many of our lives as consumers.

As Deputy Chair, my priority is increasing Weebit's engagement with Australian investors, regulators, and the local deep-tech industry. With very few sector peers on the ASX, market education is crucial to ensure our technology, growth opportunity, and progress milestones are well understood by the investment community. My appointment also enables Dadi to focus on supporting Coby and the leadership team on strategy.

In parallel, we are continuing to enhance operational and corporate frameworks as we prepare the business for its next phase of growth.

I'll now hand over to Coby for today's CEO presentation.



Approaching the Tornado

*From early adopters towards
volume deployment*

November 2025 Investor Presentation

Annual General Meeting of Shareholders 2025



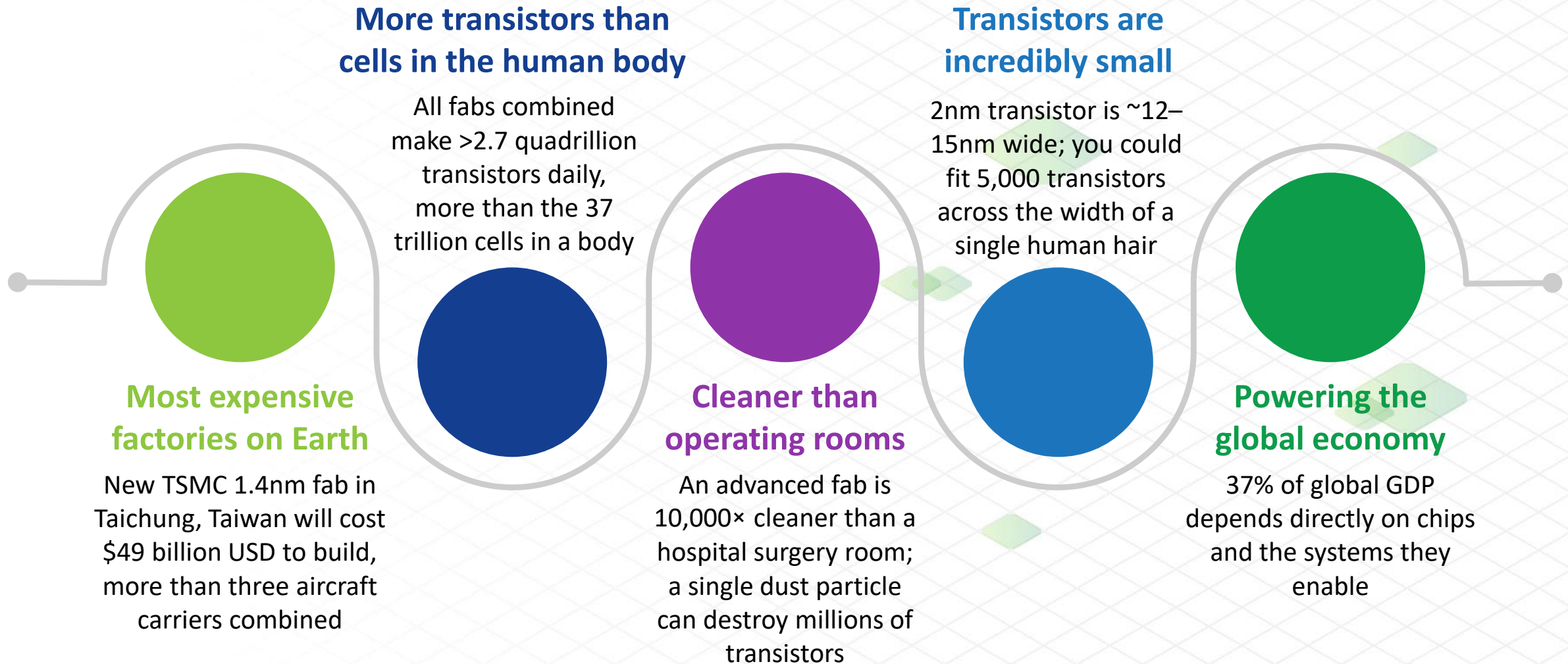


Industry and Corporate Update

Dadi Perlmutter, Chairman

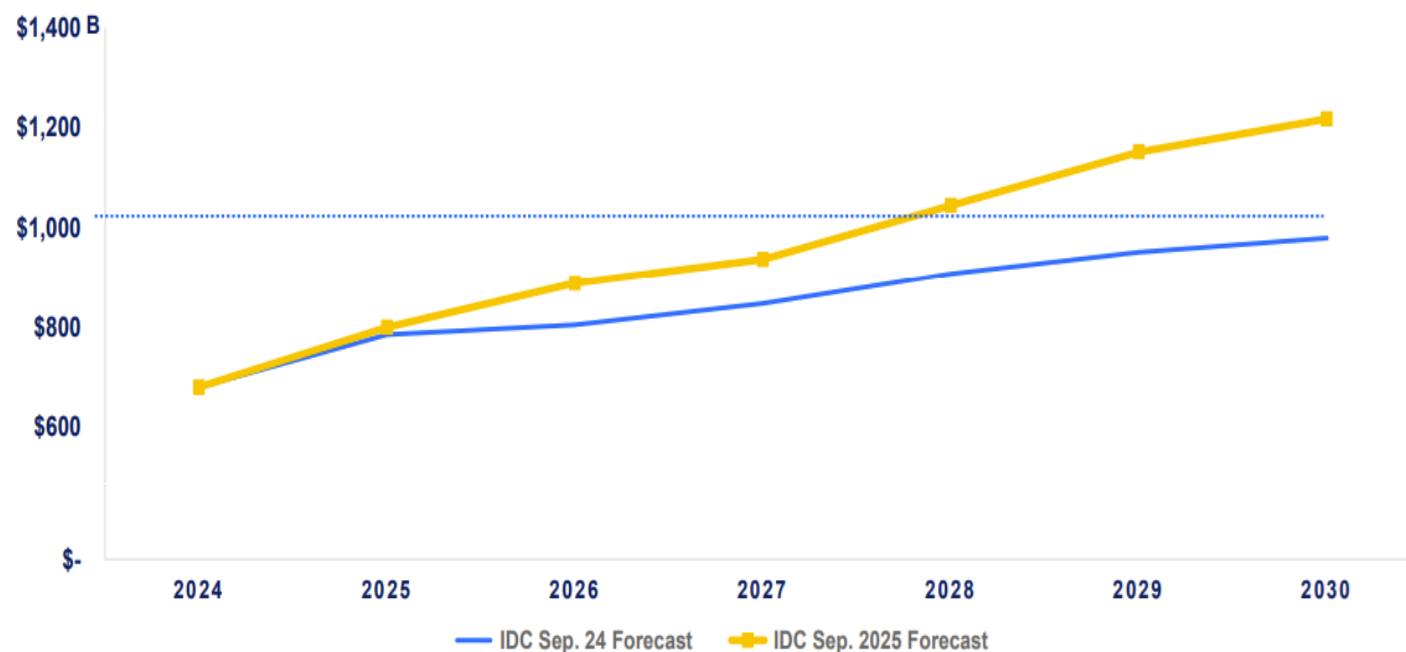


| Did you Know? Some Facts about Semiconductors



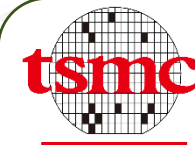
Semiconductors: A Snapshot

AI Supercycle Accelerates \$1T Semiconductor Revenues Earlier Than Expected (~2 years)



Source: IDC CY2Q25 Semiconductor Application Forecast, Sep. 2025

Source: Company Filings, IDC Estimates



TSMC Intends to Expand Its Investment in the United States to US\$165 Billion to Power the Future of AI



Nvidia to invest \$5bn in Intel after Trump administration's 10% stake



India is betting \$18 billion to build a chip powerhouse.

| Impact of Geopolitical Tensions on Semiconductors



| Impact of Geopolitical Tensions on Semiconductors



Strong and Experienced Board and Management Team

Coby Hanoch
CEO



David (Dadi) Perlmutter
CHAIRMAN



Anne Templeman-Jones
DEPUTY CHAIR



Atiq Raza
NON-EXEC. DIRECTOR



Yoav Nissan-Cohen
NON-EXEC. DIRECTOR



Naomi Simson
NON-EXEC. DIRECTOR



Ashley Krongold
NON-EXEC. DIRECTOR



Alla Felder
CFO



Ishai Naveh
CTO



Ilan Sever
VP R&D



Lilach Zinger
VP CUSTOMER SUCCESS



Issachar Ohana
CRO



Eran Briman
VP MARKETING & BUSINESS DEV.



Governance Update

Preparing to be an ASX 200 Company

Policy & Compliance

Upgrading internal governance policies and frameworks



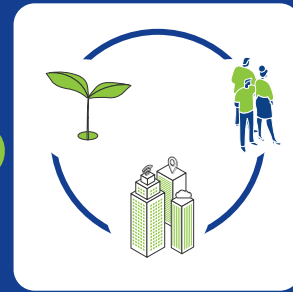
Risk & Resilience

Business Continuity and Disaster Recovery planning including Enterprise Risk Assessment (ERA)



Sustainability & ESG

Launched ESG reporting and disclosure initiatives



Board Leadership

Australia-based Anne Templeman-Jones appointed Deputy Chair to strengthen local representation/oversight





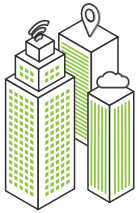
Business Overview

Coby Hanoach, CEO



| Weebit Overview: Leading Vendor of ReRAM IP

Advanced Non-Volatile Memory (NVM) Now Entering Production



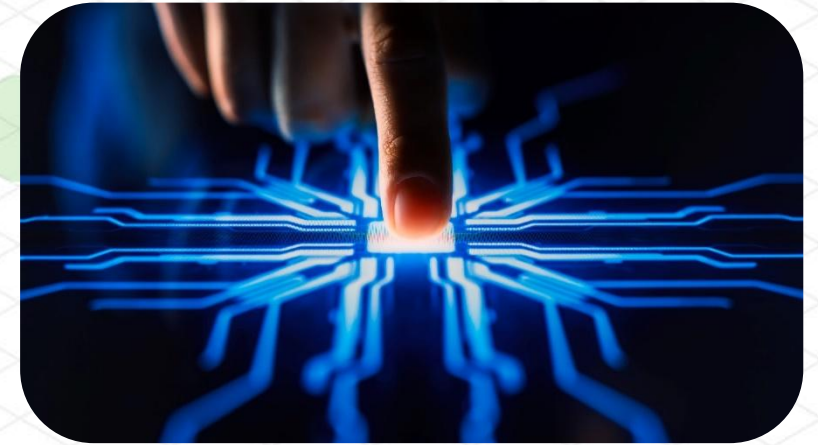
Founded: 2015

Located: Israel, France & US
60 employees (~90% engineers/scientists; 17 PhDs)



Multiple commercial deals

Including tier-1 IDM onsemi;
ongoing discussions/evals with >20 foundries/IDMs/product companies



Business model

IP licensing to semiconductor companies & fabs



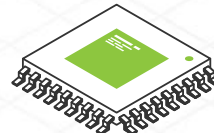
Fast-growing markets

Edge AI, automotive, microcontrollers, power management/analog ICs...



R&D partner

CEA-Leti, a leading microelectronics research institute



Proven, protected technology

Fully qualified per JEDEC & AEC-Q100 standards; available for chip designers; >90 patents/applications

ASX: WBT

S&P/ASX 300

MSCI
Global Small Cap Index

| Embedded Non-Volatile Memory (NVM) is Strategic IP



Ubiquitous across Growing Applications

- AI, IoT, automotive, industrial, medical, consumer, defense
- Enables device intelligence, security, and resilience



Foundational to Modern Electronics

Critical for:

- Firmware and code storage
- AI weights and data tables
- Configuration data and ID keys
- Data logging



Drives Differentiation

- Performance: faster operation, better endurance
- Power consumption: Both run-time and sleep power
- System integration: Enabling single-chip solutions
- Secured: Prevent chip hacking



Strategic across the Supply Chain

- Designers: simplify architecture; reduce BOM
- Foundries: added value vs. competition
- OEMs: smarter, more secure products
- Ecosystem: differentiate and monetize IP

A Brief History of Non-Volatile Memory (NVM)

~2010

Flash Scaling Issues
Known



Microelectronic Engineering
Volume 86, Issue 3, March 2009, Pages 283-286



Future challenges of flash memory
technologies



The Memory Guy Blog

Jim Handy, Objective Analysis, on Semiconductor Memories

Home About Jim Handy Privacy Policy

Month: June 2012

The End of Flash Scaling

Alternatives Emerge

FeRAM

MRAM

CBRAM

ReRAM

PCM

3Dxpoint

Optane

Too expensive
Difficult to implement
Exotic materials

~2020

Choices Narrow

MRAM

Mass
Production

ReRAM

Multiple
Companies
Developing

2024

Superior
Tech Wins

MRAM

Magnetic
Issues

ReRAM

In Production;
Vendors
Shake Out

TSMC redirects its
roadmap from MRAM
to ReRAM

Today

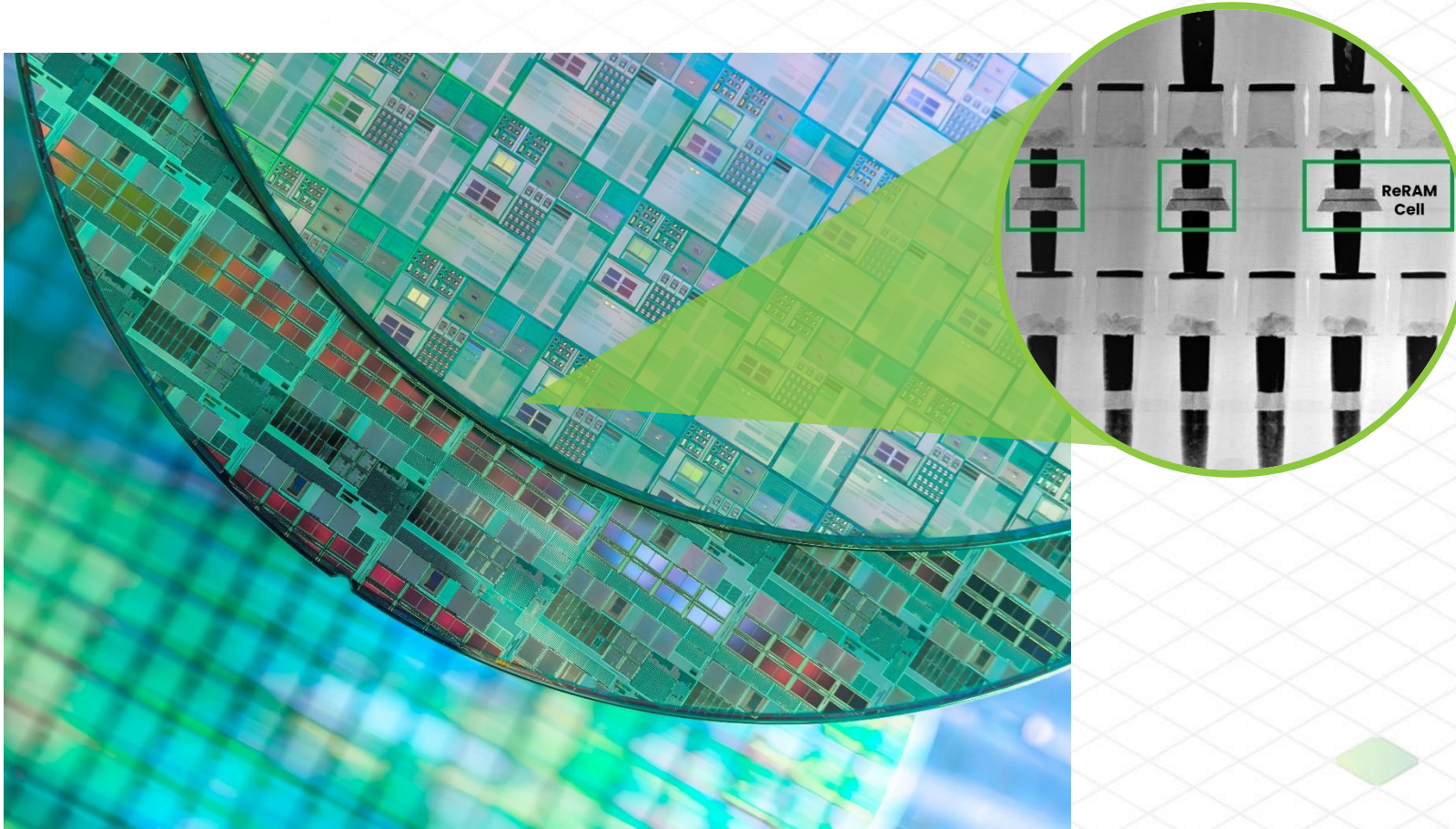
ReRAM is Chosen
Flash Replacement



Weebitnano
THE NEXT NVM IS HERE

**The only
independent
ReRAM
supplier**

ReRAM – Innovative NVM for a New Era



Weebit ReRAM

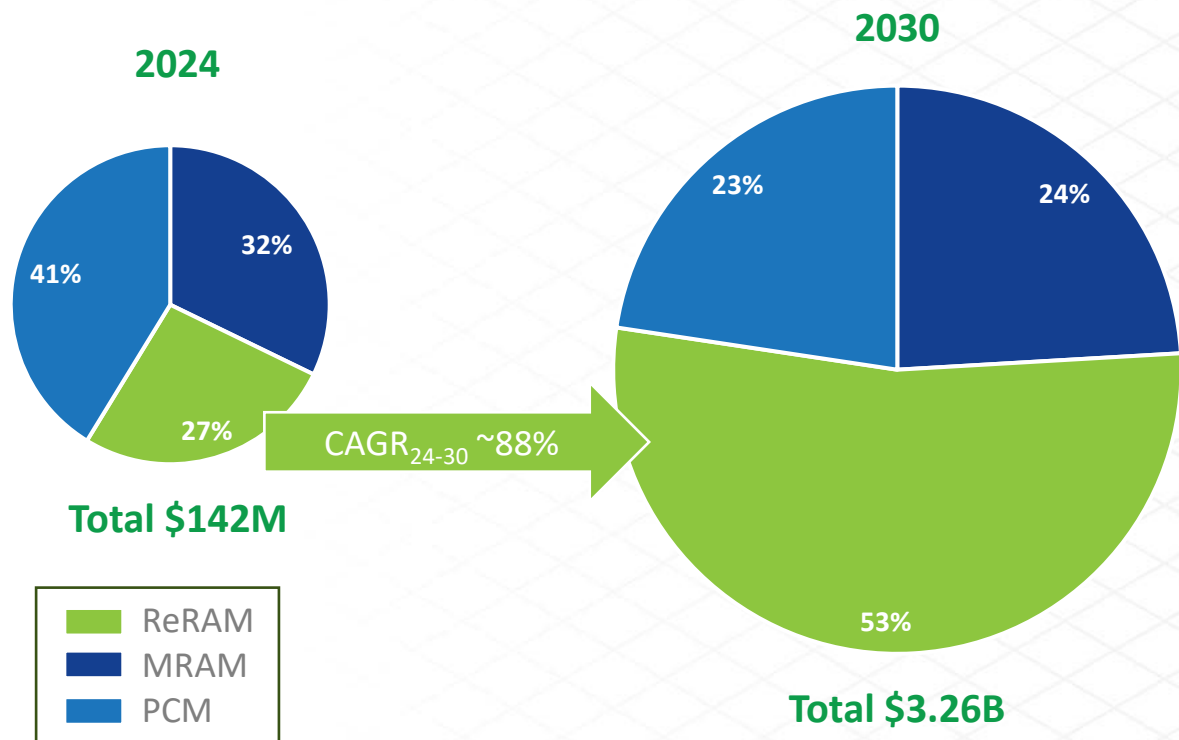
- ❖ Scales to advanced geometries where flash can't
- ❖ Ultra-low power
- ❖ Cost-effective
- ❖ Easy to integrate
- ❖ Highly reliable even at high temperatures
- ❖ Immune to EMI and radiation
- ❖ Fully qualified per JEDEC and AEC-Q100 standards
- ❖ >90 patents/applications

Weebit: The only independent provider of qualified ReRAM

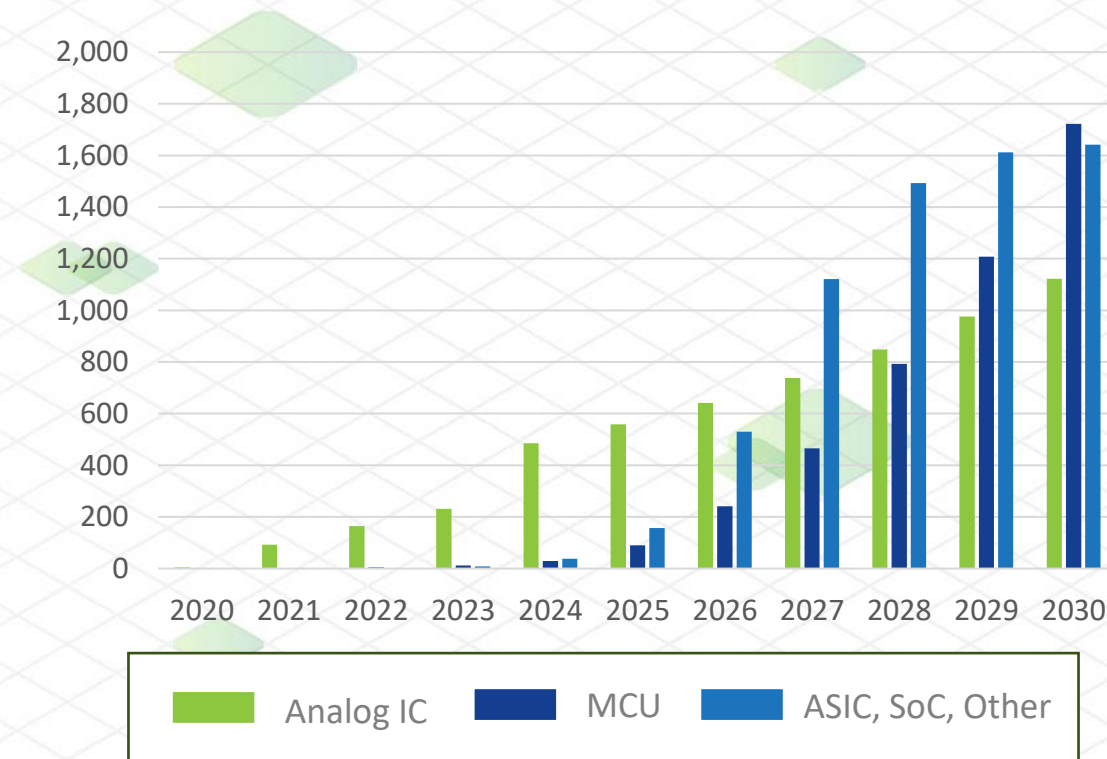
The Rise of ReRAM

45x growth in 6 years

Embedded Emerging NVM Market (in \$M)



Embedded ReRAM Market Volume
(# of Dies, in M units)



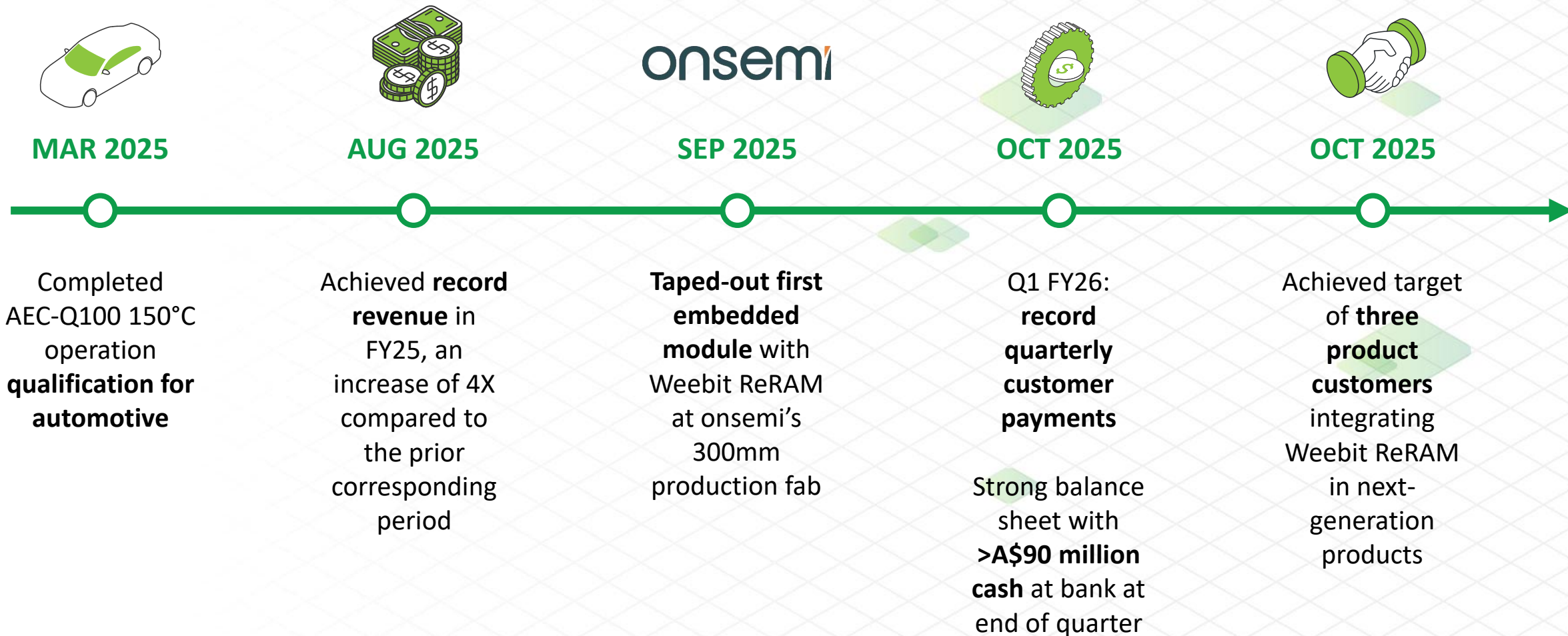
Note: The embedded emerging NVM market size is evaluated based on assumptions of the average chip area occupied by a given memory technology (Yole)



Recent Achievements



Recent Highlights



Tape-out of Embedded ReRAM Test Chips at onsemi

Marks major milestone in completing technology transfer of Weebit ReRAM to onsemi's advanced BCD process

onsemi

onsemi provides intelligent power and sensing solutions with a focus on automotive and industrial

500

Fortune 500®
company
S&P 500® index

#2

Global position in
silicon power
solutions

100

Nasdaq 100®
company

\$7.1B

2024 revenue
(USD)

onsemi was spun out of Motorola as ON Semiconductor in 1999

onsemi data from the onsemi Quarterly Investor Presentation Fourth Quarter 2024

- ❖ ReRAM being integrated into **onsemi's Treo™ platform**
 - ◆ Combines power management, compute & sensing on a single die
 - ◆ Ideal for high-temp automotive, industrial & data-center
 - ◆ Manufacturing 65nm BCD in a 300mm US production fab
 - ◆ Brings **more intelligence** to a range of onsemi's products
 - ◆ Provides **power-efficiency and cost-effectiveness**
- ❖ Collaboration progressing rapidly
 - ◆ Validated Weebit ReRAM on multiple wafer lots, demonstrating **solid performance and reliability**
 - ◆ Test chips will be used for final testing and qualification ahead of anticipated volume production
 - ◆ **Qualification expected in 2026**

**IDMs manufacture their own products,
shortening time-to-production and potentially
accelerating time-to-royalties**

Weebit ReRAM in Automotive

Increasing number of semiconductors per car

- ❖ ~1,000 - 3,000 chips per car; an electric Audi or Volkswagen contain up to 8,000 chips*

Automotive in need of new NVM technologies

- ❖ Growing needs for emerging NVM: code storage, trimming, data logging...
- ❖ Can effectively scale to advanced process nodes, <28nm

Parameters of Weebit ReRAM align with automaker specifications

- ❖ High-temp reliability, EMI immunity, high endurance, fast switching speed, longevity, secured

Completed AEC-Q100 150°C Operation Qualification

Key to getting designed into automotive MCUs & other components

- ❖ Demonstrated **stability at 150°C operation for up to 100K endurance cycles****
- ❖ Including cycling and post-cycling high-temp data retention

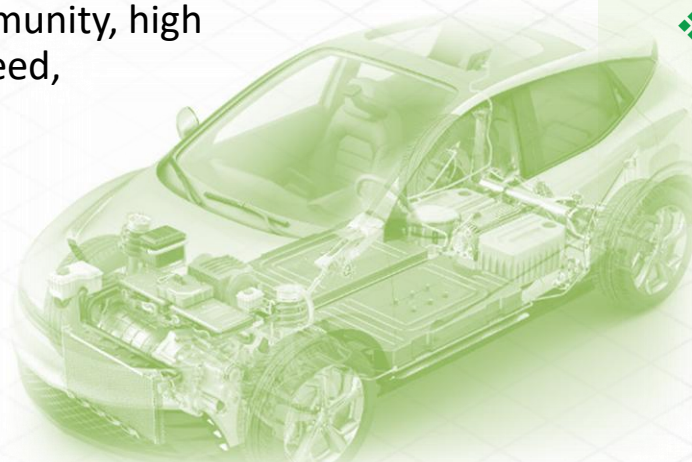
Beyond Automotive

- ❖ Many **industrial** and **IoT** applications require high-temp and extended endurance
- ❖ With AEC-Q100 Weebit can service over 99% of applications

Completed qual in SkyWater S130 using module with 1T1R cell architecture

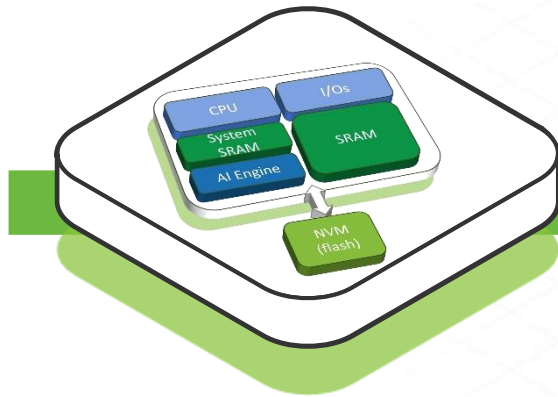
* <https://www.audi-mediacycenter.com/en/press-releases/semiconductors-are-becoming-the-neurons-of-our-cars-16053>

** Flash equivalent



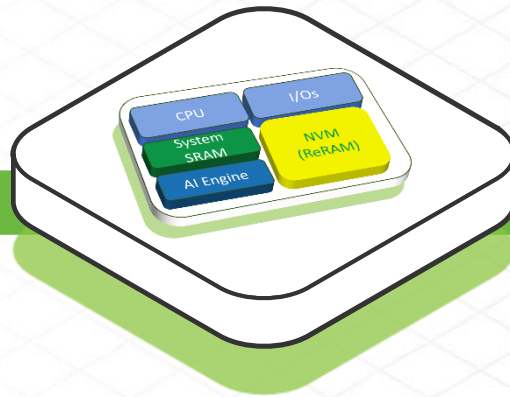
| The Journey to New AI Architectures with ReRAM

TODAY'S TWO CHIP SOLUTION



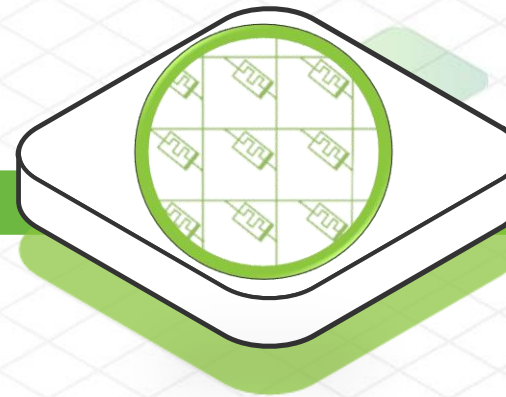
- NVM (flash) on separate chip
- Wasteful in terms of size & cost
 - Prohibitive power
 - Constrained data bandwidth and performance
 - Insecure, vulnerable to hacking

SINGLE CHIP NEAR-MEMORY COMPUTE



- External NVM eliminated
- Eliminates most data movements → Low-power
 - More efficient storage: **4X greater capacity** than SRAM

IN-MEMORY COMPUTE WITH ReRAM



- Computation performed *within* memory arrays
- **Completely eliminates** data traffic → Ultra-low power
 - Enables **Analog Compute** → Store larger models efficiently

NEUROMORPHIC COMPUTE WITH ReRAM



- Future systems will mimic brain behavior
- Fast real-time processing on **massive amounts of data**
 - Three orders of magnitude **(x1000) better energy efficiency**

ReRAM-based architectures are central to the transformation of AI, **bringing memory and compute together** for faster, brain-like intelligence

Progress to Goals so Far in CY 2025

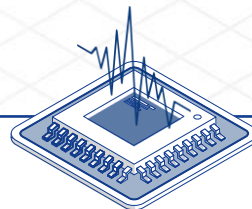
Goals stated at the 2024 AGM



Fabs

Three new
licensing
agreements

Expect 2/3*



Products

Three new
agreements with
product companies

Completed



Qualification

Complete
qualification at
DB HiTek

On track

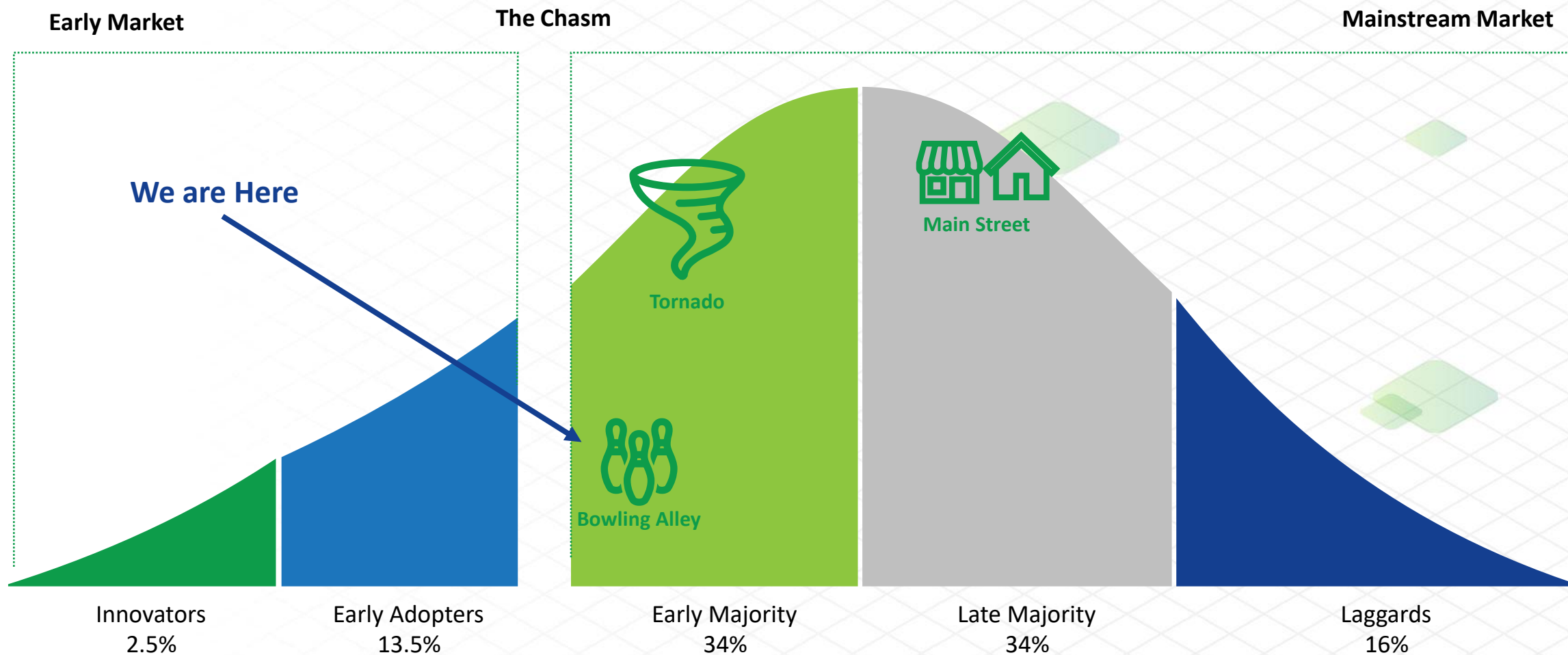
* onsemi was announced in Jan 2025. Weebit is now targeting another agreement in CY25 and one will slip to 2026.



Opportunities Ahead



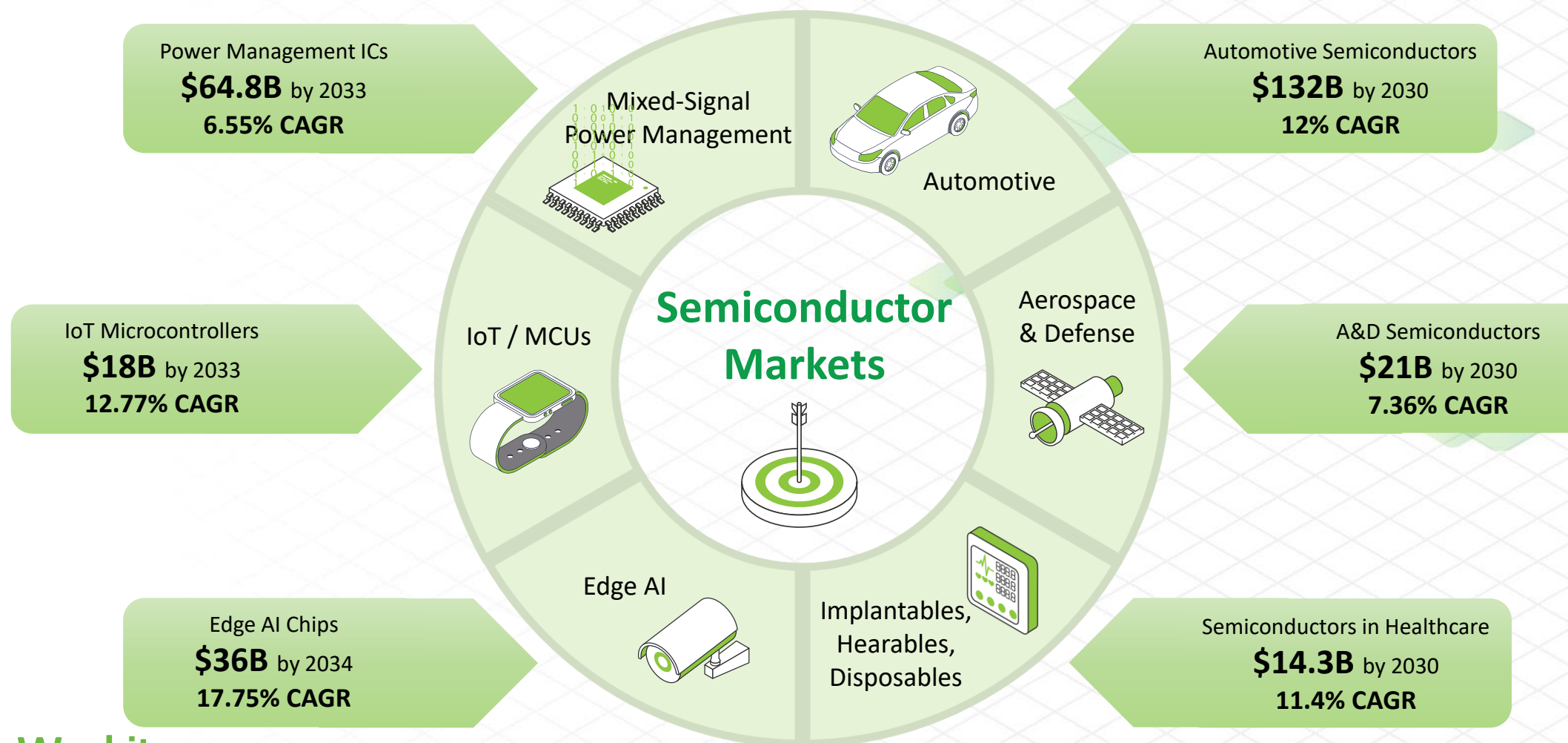
Heading for the Tornado



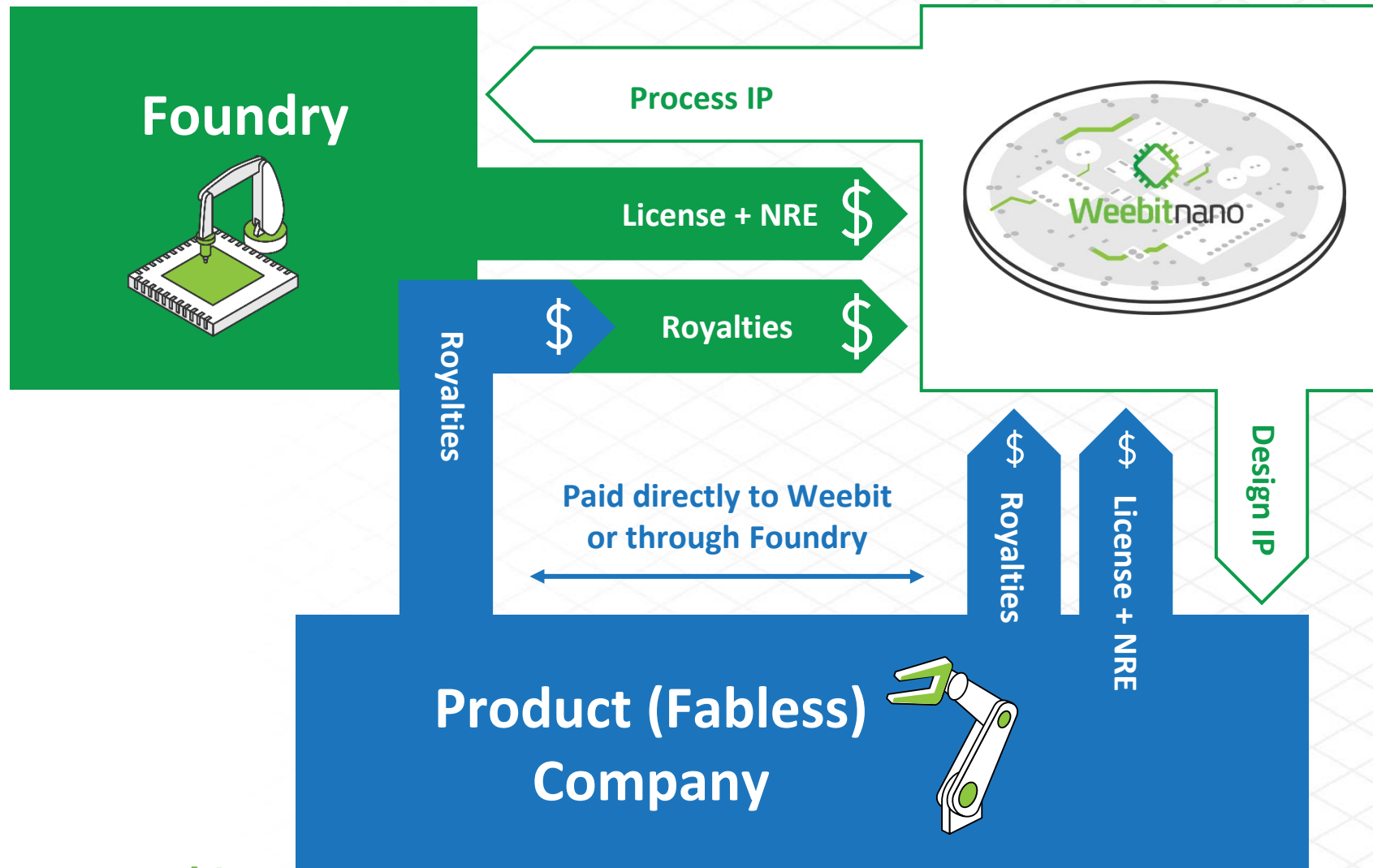
Foundation Diagram courtesy of <https://www.ignitionframework.com/crossing-the-chasm-theory-how-to-market-sell-and-improve-your-new-invention/>

Addressing Fast-Growing Semiconductor Markets

>US\$265 Billion TAM in Customer Markets



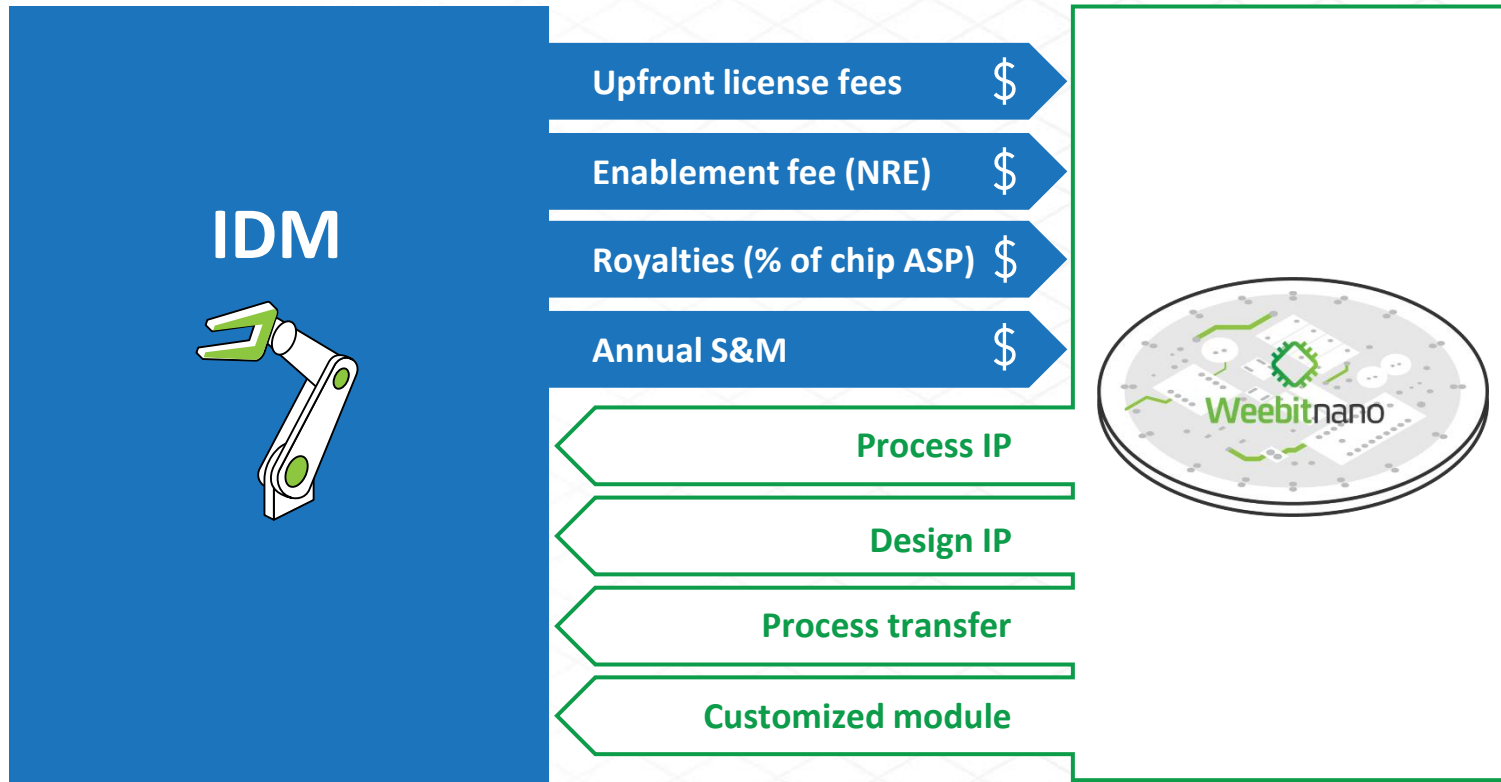
IP Business Model – Foundries & Fabless



Benefits of IP Model

- ❖ Low COGS
 - ◆ Direct costs are associated with supporting and maintaining IP
 - ◆ No inventory
- ❖ High gross and operating margins
 - ◆ Once IP is created, it gets sold multiple times
 - ◆ Royalties go directly to the bottom line

IP Business Model – Benefits with IDMs

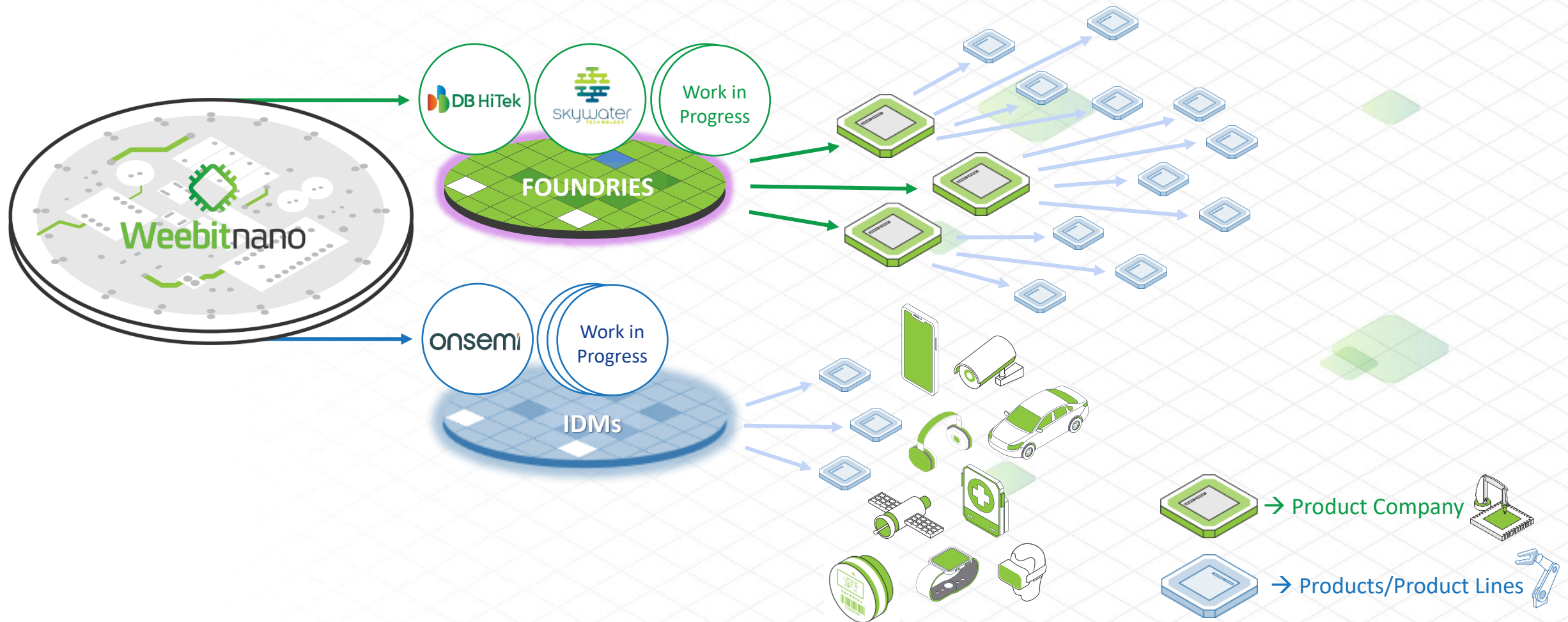


❖ IDMs design, manufacture and sell their own devices

- ◆ Develop products quickly for target end markets
- ◆ Can integrate IP in multiple designs / products

Accelerated
time-to-market and
time-to-revenue

| Every Foundry Deal Represents Multiple Customer Opportunities



Foundations for Growth are in Place





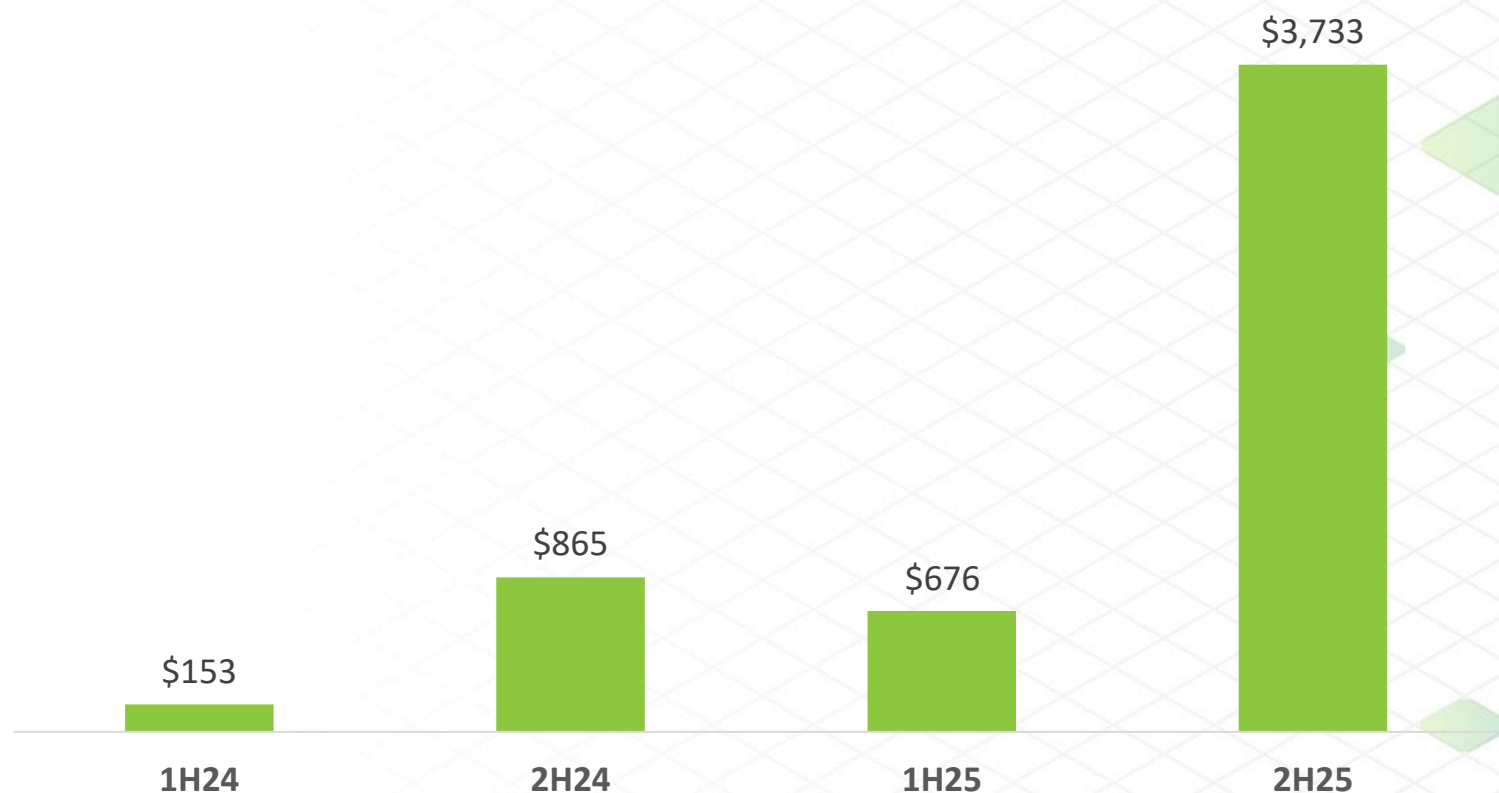
Financials

Alla Felder, CFO



Revenue Growth Accelerating

Half-Yearly (FY) Revenues (in A\$M)

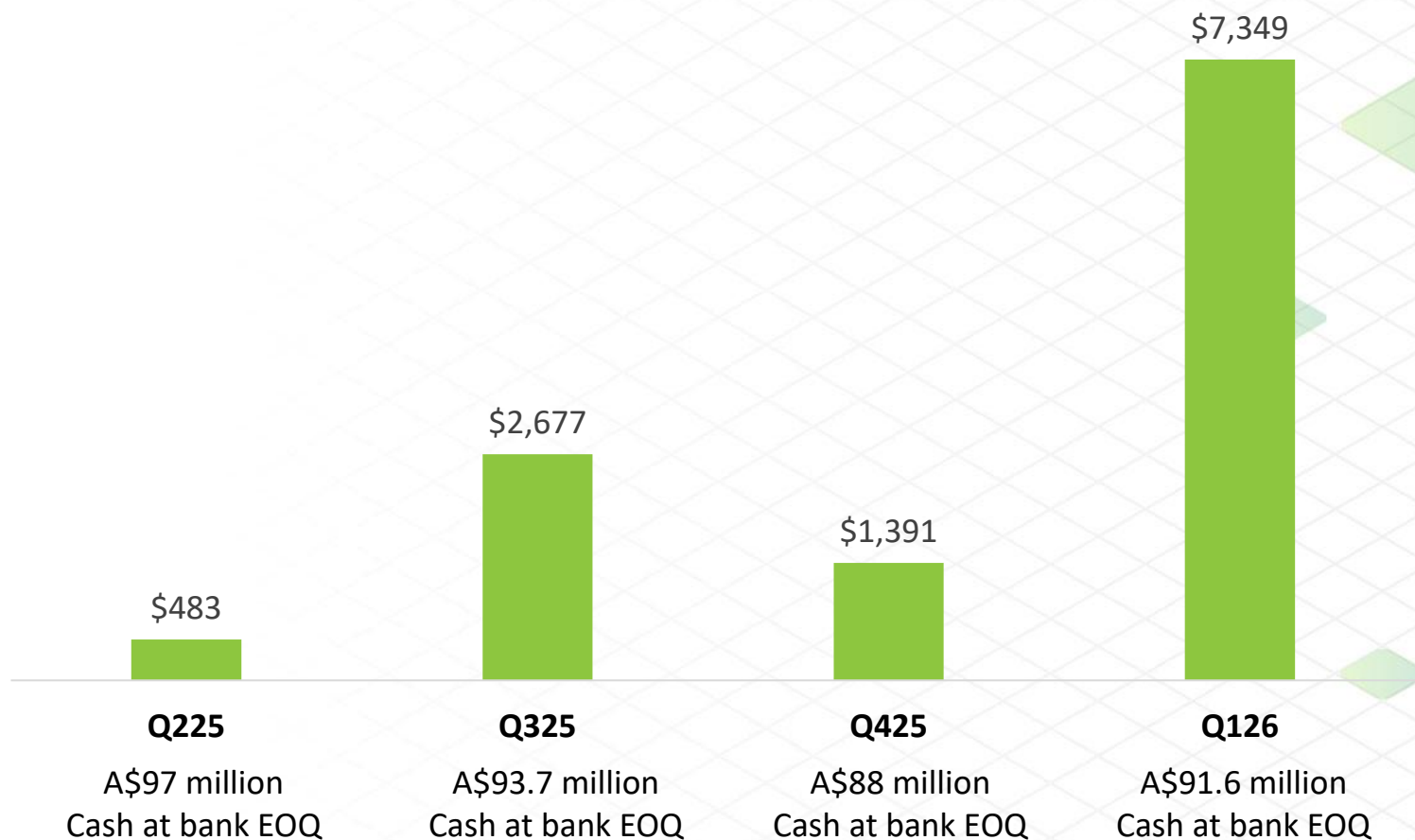


- ❖ Annual revenue
 - ◆ \$1 million in fiscal 2024
 - ◆ \$4.4 million in fiscal 2025
- ❖ Revenues expected to continue to build based on new licensing and design agreements

Note: Past revenues and cash income do not necessarily reflect on the future

| Record Customer Receipts

Quarterly (FY) Cash Flows (in \$A Thousands)

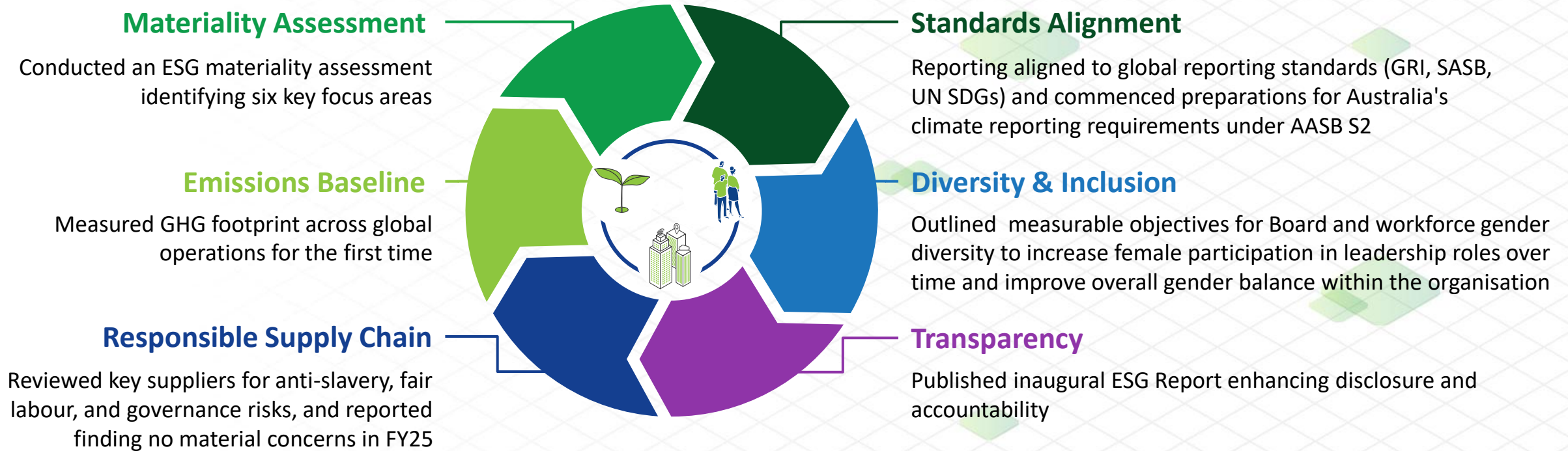


- ❖ Quarterly customer receipts represented a record A\$7.3 million in Q1 FY26
- ❖ Mainly milestone-based payments from existing customers
- ❖ Other notable cash inflows included A\$4.1 million relating to 2024 R&D tax rebate for Weebit's French subsidiary

Note: Past revenues and cash income do not necessarily reflect on the future

ESG Report Highlights

Progressed and formalised approach to ESG management and reporting



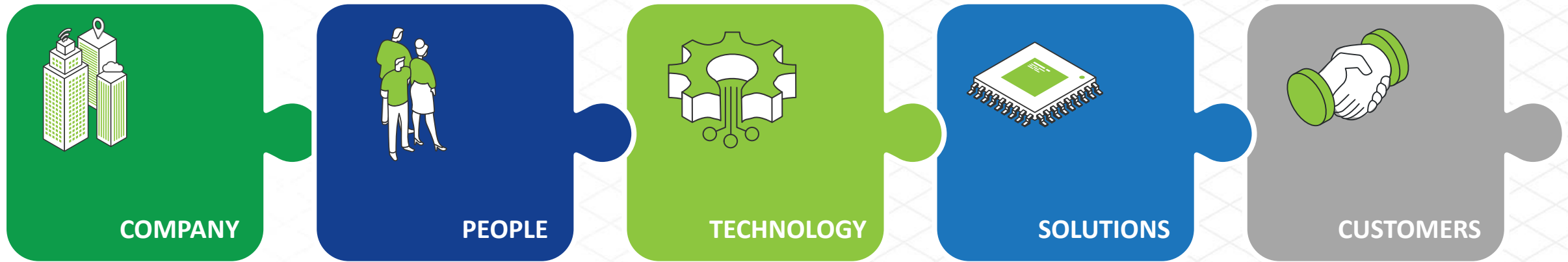


Looking Ahead

Coby Hanoach, CEO



Weebit's Differentiation



- Only **independent** provider of **qualified** ReRAM technology
- **ASX 300** (ASX:WBT)
- >A\$90M cash e/o 1Q FY26

- Located: Israel, France, US
- >60 personnel (~**90% engineers**; 17PhDs)
- R&D partner  
- Experienced management
- Continued scaling up of the team and required infrastructure

- Low-power, cost-effective, reliable NVM
- Scales where flash can't
- Targeting **fast-growing markets**: edge AI, automotive, power / analog ICs, MCUs, ...
- Qualified and ready for production

- Licensing **Semiconductor IP** for embedded markets
- Research towards **IMC and neuromorphic**
- Exploring solutions for future **discrete markets**
- Unrivalled design team delivers **high-quality IP**; enables **fast time-to-market** for customers

- **Multiple commercial deals**, including tier-1 IDM onsemi and top-10 foundry DB HiTek
- 
- Ongoing discussions and evals with **>20 vendors** (foundries, IDMs, product companies)
- **Several products in design** with Weebit ReRAM

WORLD-RENOWNED BOARD OF DIRECTORS

David (Dadi) Perlmutter
CHAIRMAN



Anne Templeman-Jones
DEPUTY CHAIR



Atiq Raza
NON-EXEC. DIRECTOR



Yoav Nissan-Cohen
NON-EXEC. DIRECTOR



Naomi Simson
NON-EXEC. DIRECTOR



Ashley Krongold
NON-EXEC. DIRECTOR



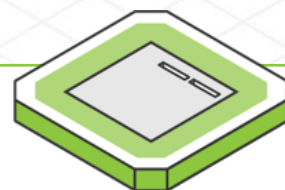
Weebit Nano Key Targets for CY 2026



Revenue

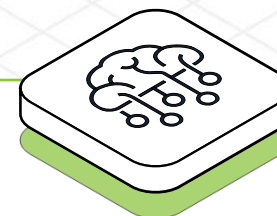
FY26 revenue goal
>\$10M*

- This represents Weebit's current goal FY26 revenue and is not a forecast.



Products

First product
company customer
tape-out



Customers

First AI customer
engagement

**Delivering on these milestones will continue to cement Weebit's position
as the leading independent provider of ReRAM technology**

Disclaimer

This presentation contains certain statements that constitute forward-looking statements. Examples of such statements include, but are not limited to, statements regarding the design, scope, initiation, conduct and results of our research and development programs; our plans and objectives for future operations; and the potential benefits of our products and technologies. In some cases, forward-looking statements can be identified by the use of terminology such as “may,” “will,” “expects,” “plans,” “anticipates,” “estimates,” “potential” or “continue” or the negative thereof or other comparable terminology. These statements involve a number of risks and uncertainties that could cause actual results and the timing of events to differ materially from those anticipated by these forward-looking statements. These risks and uncertainties include a variety of factors, some of which are beyond our control. All forward-looking statements and reasons why actual results may differ are based on information available to us when initially made, and we assume no obligation to update these forward-looking statements or provide reasons why actual results might differ or the information set forth herein.

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Thank You!

www.weebit-nano.com



 **Weebitnano**
THE NEXT NVM IS HERE